MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(UGC-AUTONOMOUS INSTITUTION)
Affiliated to JNTUA,Ananthapuramu&Approved by AICTE, NewDelhi
NAAC Accredited with A+ Grade, NIRF India Rankings 2025 - Band: 201-300 (Engg.)
NBA Accredited - B.Tech. (CIVIL, CSE, ECE, EEE, MECH), MBA& MCA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

ACTION TAKEN REPORT BASED ON FEEDBACK FROM STAKEHOLDERS

Academic Year 2025-2026

S.No.	Feedback	Action Taken	Outcomes/Achievements
1	Industry experts have requested that more industrial visits be organized to enhance students' practical exposure and interaction with industry.	In response to the feedback, the institute procured two dedicated executive buses to facilitate industrial visits across India, ensuring safe and efficient transportation for students.	As a result, industrial visits were successfully organized to five companies, benefiting a total of 225 students through enhanced experiential learning and industry exposure.
2	Students are requested to seek industry involvement in internships and projects to gain real-time experience.	We executed three Memoranda of Understanding (MOUs) with industry partners.	Students are undergoing internships with the industries where Memoranda of Understanding (MOUs) were signed.
3	Alumni and industry experts emphasized the need for students to prioritize upskilling in emerging technologies and industry-relevant domains.	In response, the department organized 08 programmes comprising hands-on training, skill development, and global certification to enhance students' competencies.	A total of 1,823 participants benefited from these programmes, leading to improved technical skills and better industry readiness.
4	Students expressed their willingness to have in-house placement training to enhance their employability skills.	In response to the feedback, the institution executed a Memorandum of Understanding (MoU) with Ethnotech Academy, Bangalore, to provide structured placement training.	A total of 255 students attended the training program, improving their preparedness for campus recruitment.

5	Students conveyed the expectation that internship opportunities in reputed institutions should be facilitated to improve practical and technical competencies.	In response to the feedback received, the department proactively identified reputed institutions offering internship opportunities and established necessary coordination. Students were guided through the application process, and continuous mentoring support was provided by faculty members to ensure effective participation and successful completion of internships.	As a result, 13 students successfully completed internships at reputed institutions such as NIT Silchar, NITTR Chennai, and NIT Puducherry, while 844 students completed internships at various companies.
6	GATE aspirant students requested the provision of a GATE Online Test Series – 2026 and suggested conducting GATE coaching classes during working hours.	A GATE Online Test Series – 2026 was procured from ACE Academy, Hyderabad, to facilitate continuous assessment through periodic mock tests. Additionally, GATE training was organized for GATE aspirant students during working hours (3:00 PM to 5:00 PM) to enhance effective participation.	A total of 15 students qualified in the GATE 2026 examination, compared to 9 students in GATE 2025, showing an increase of 6 qualified students.

Faculty Incharge

HoD/CSE

Head of the Department
Computer Science & Engineering
Madanapalle Institute of Technology & Science
MADANAPALLE-517 325.

PRINCIPAL
Madanapalle Institute of
Technology & Science
MADANAPALLE-517325

Department of Computer Science & Engineering

List of Industrial Visits for the Academic Year 2025–2026

S.No.	Date	Industry Name & Location	No. of students Visited
1	13-03-2026	National Atmospheric Research Laboratory (NARL) & Gadanki, Pakala, Andhra Pradesh 517112	49
2	15-05-2026	Samvardhana Motherson Reydel Companies (SMRC), & Tamil Nadu, India	43
3	19-05-2026	Centre for Development of Advanced Computing (C-DAC), & Bengaluru	52
4	21-05-2026	Cimtrix Systems Pvt. Ltd., & Bengaluru	39
5	22-05-2026	Centum Electronic Limited & Bengaluru	42

HoD/CSE

Computer Science & Engineering
Madanapalle Institute of Technology & Science
MADANAPALLE-517 325.

MITS
MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
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NBA NATIONAL BOARD OF ACCREDITATION CIVIL,CSE,ECE,EEE,MECH, MBA & MCA
Accredited by NAAC A+ Grade
nirf NATIONAL INSTITUTIONAL RANKING FRAMEWORK
MITS ranked in the band 251-300-NIRF 2022

A Report on Industrial Visit To "National Atmospheric Research Laboratory (NARL)" Gadanki, Pakala, Andhra Pradesh 517112 Organized by Department of Computer Science & Engineering on 13.03.2026



Report Submitted by: Dr. R. Nidhya, Professor, Department of Computer Science and Engineering, MITS

Faculty Members Accompanied : Dr R. Sudhakar Associate Professor/CSE Mrs. V Nirupa Assistant Professor/CSE
Total no. of Participants 49

One-day Industrial Visit to National Atmospheric Research Laboratory (NARL) was organized for the II Year B.Tech [CSE] students on 24th November 2023. The Industrial Visit to NARL started at 6.30 AM by college bus and reached NARL by 10.30 AM. The students along with faculty members visited the NARL facilities between 10.30 AM and 1.30 PM.

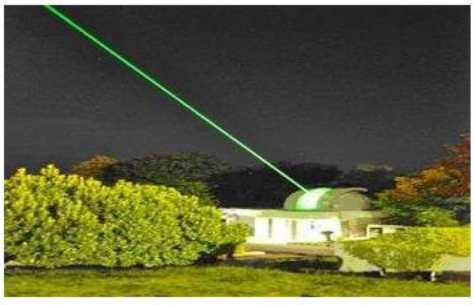
ABOUT NARL:

NARL is an autonomous research laboratory fully funded by the Department of Space, Government of India and involved in carrying out fundamental and applied research in Atmospheric and Space Sciences. It had its humble beginning in 1992 as the National Mesosphere-Stratosphere-Troposphere (MST) Radar Facility. Over the years several complementary techniques such as Rayleigh/Mie lidars, wind profilers have been added. NARL is administered by a Governing Council with Secretary, DOS as the Chairman and Director, NARL as member secretary. The Governing Council sets broad policy guidelines for NARL. A Scientific Advisory Committee consisting of eminent scientists in the field of atmospheric science, monitors the research activities and progress of NARL and provides future directions.

Places Visited in NARL:

Rayleigh Doppler Lidar :

The project Rayleigh Doppler Lidar (RDL) uses a high energy laser with 30W power @532nm and 50Hz repetition rate to get atmospheric backscattering from molecules and particles. A 750mm telescope in Newtonian configuration is used to collect the signal from the atmosphere and a Fabry Perot Interferometer as frequency discriminato r is employed to get the radial winds. A few photographs of Pic 1 and 2 of the system are shown. The Project is completed, and Zonal and Meridional winds are obtained between 10-50km. Sample results are shown in the Fig 1 and 2. The winds derived from this indigenously developed system have compared well with near simultaneous GPS Radio Sonde winds. The RDL system is regularly operated. The system capability of moving the telescope in 0–360-degree azimuth and 0-45 degrees off-zenith is fully utilized to get the temperature structure.



2. **GPS Radiosonde:**

NARL launches GPS radiosonde (RD-11 G, Meisei make) every day around 1730 LT (12 UTC). It provided profiles of Pressure, Temperature, Relative humidity, Wind Speed and Wind Direction up to the balloon burst altitude typically 30 -35 km. These have been launched regularly since April 2006. The RD - 11G ground station is designed for the Upper Air Observing System using the D - GPS (Differential Global Positioning System). The GPS receiving device in a Radiosonde tremendously improves observation accuracy, thus allowing the GPS ground station to be simplified and compact. The RD-11 G Upper Air Observing System consists of a radiosonde recei ving antenna system, GPS receiving antenna, GPS Sonde Receiver, and the data processor, PC computer which is connected to a networking subsystem via a TCP/IP. The system is also provided with the Base Line Checker (BLC) used for ground check before launching a radiosonde. The GPS receiving device in a radiosonde obtains information on the direction and the velocity of the GPS satellite with the frequency deviation of radio signals from the satellite due to the Doppler shift and transmits the positioning inf ormation of the radiosonde to the RD-11 G ground station. Upon receiving radio signals from the radiosonde, the RD - 11G ground stations processes the data obtained by the radiosonde and GPS satellites to observe wind direction and wind speed in the upper air .



3. **MST Radar:**

The MST radar is an excellent system used for atmospheric probing in the regions of Mesosphere, Stratosphere and Troposphere (MST) covering up to a height of 100 Km. It is also used for coherent backscatter study of the ionospheric irregularities above 90 km. MST radar is a state-of-the-art instrument capable of providing estimates of atmospheric parameters with very high resolution on a continuous basis, which is essential for the study of different dynamical process in the atmosphere. Radar operates at 53 MHz with a peak power of 2.5 MW. The phased antenna array consists of two orthogonal sets, one for each polarization of 1024 three element Yagi-Uda antennas arranged in a 32 x 32 matrix over an area of 130 m x 130 m. The two sets are co-located with pairs of crossed Yagis mounted on the same set of poles. The array is aligned along the geomagnetic axes to enable the radar beam to be transverse to the Earth's magnetic field for ionospheric backscatter application. The array of either of the polarizations is illuminated using 32 transmitters of varying power, each feeding a linear sub-array of 32 antennas. The power distribution across the array follows an approximation to modified Taylor weighting in both principal directions. The radar beam can, in principle, be positioned electronically at any look angle within ± 200 off-zenith in the East-West and North-South planes. It is possible to transmit both coded and un-coded pulses with pulse repetition frequency in the range of 62.5 Hz to 8 KHz, with a maximum duty of 2.5 %. Coded and un-coded pulse can be varied from 1 to 32 µs with a baud length of 1 µs providing a range resolution of 150 m. The radar operates under instruction from a PC based radar controller that executes an experiment according to the experimental specification set by the scientists. Both time series and power spectrum data canbe recorded on-line. The recorded data can be processed offline to derive various atmospheric parameters.



4. **Automatic Weather Station:** The automatic weather station consisting of 5 sensors and tipping b ucket rain guage is installed at NARL. The sensors measure pressure, temperature, humidity, wind speed and wind direction. Except the pressure sensor and the rain gauge, the other sensors are mounted on a 3 - m tower. Temperature compensated piezo-resistive pressure sensor is used to measure the pressure with a resolution of 0.1 mb. For humidity measurement, a thin film capacitance sensor is used which provides an accuracy of ± 3%. RTD type sensor is used to measure temperature with a resolution of 0.1o C. 3 -cup rotor type sensor is used to measure wind speed with an accuracy of ± 1%. Potentiometer type sensor is used to measure the wind direction with a resolution of < 1 o. A tipping bucket rain gauge provides rain rate information with a resolution of 0.5 mm.



5. HPC Facilities:

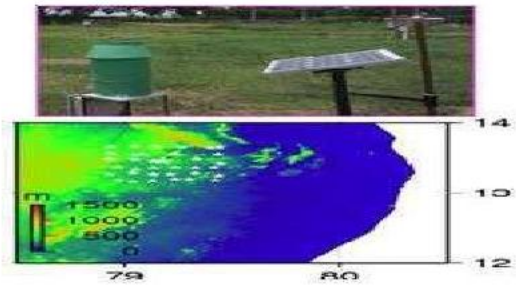
We visited the High-Performance Computing (HPC)Lab Facility in NARL, high performance computing cluster facility for accelerating analysis of genomics and transcriptomics data is available. This facility comprises with the following Configurations

- 96 Parallel processing
- 200TB of RAM
- 200GBPS Connectivity
- 2 petabytes of Data Storage
- 32GPU's



6. Rain Guage Network:

One of the vital assessments after the launch of any Microwave and Infrared (IR) remote sensing satellite is to validate of the derived products (ex. rainfall estimates), preferably by comparing with the products obtained by ground-based instruments by considering them as reference. In this regard, to validate the rainfall parameters retrieved from Megha-Tropics mission, NARL is chosen as a super-validation site and ameso-rain gauge network extending from 78.9° E to 79.4° E and 13.1° N to 13.6° N, covering an area of 50×50 km2was established during 2011-12. This rain gauge network , which is centred on Gadanki, includes 36 rain gauges arranged in nearly a square grid with a spatial resolution of ~10 km. Several factors were considered while choosing the location for the rain gauge installation, like its suitability for rain measurement, safety of the instrument, accessibility to the location, coverage of BSNL network, etc. The rain gauges are of tipping bucket type having bucket volume of 0.2 mm with one minute temporal resolution. There is a provision to store the data at the site and also to transfer the data to a server located at NARL through GPRS technology.



Conclusion:

The students observed the working environment of the NARL by undergone the visit. They experienced the usage of the technology in practical aspects. The visit was more interactive with effective learning and the students were made to learn the innovative technologies used in NARL. We extend our sincere thanks to the Management, Principal, Vice Principal and Head of the Department for their support in arranging & organizing the industrial visit.

Outcomes of Industrial Visit:

- Students gained knowledge on real time OS implementation in research area.
- Students explored about super computers and its practical working functions.
- Students gained knowledge on real-time application areas of Python, Java, SQL database and analysed their importance in scientific area.
- Students understood the cyber security involvement in space research.

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A Report on Industrial Visit to "Samvardhana Motherson Reydel Companies (SMRC), Tamil Nadu, India". Organized by Department of Computer Science & Engineering on 15.05.2026





Report Submitted by: Mr. B. Anandaraj, Assistant Professor, Department of CSE.

Faculty Members Accompanied: Dr. D. J. Ashpin Pabi, Associate Professor, Department of CSE; Mr. B. Anandaraj, Assistant Professor, Department of CSE

Total no. of Participants: 43

Mode of Conduct: Offline

Report Received on 22.05.2026.

A one-day industrial visit to the **Samvardhana Motherson Reydel Companies (SMRC)** was organized for First Year B. Tech (CSE) students on 15th May 2026. The group departed from the college at 4:30 AM by bus and arrived at SMRC at 10:30 AM. The students along with faculty members visited the **Samvardhana Motherson Reydel Companies (SMRC)** facilities between 10:30 AM to 2 PM.

ABOUT SMRC

Samvardhana Motherson Reydel Companies (SMRC) is a global leader in automotive interior systems and modules. The company supplies innovative interior solutions to several automobile manufacturers.

Major Products

- Cockpit Modules
- Instrument Panels
- Door Panels
- Console Modules
- Decorative Interior Parts

These products enhance vehicle functionality, aesthetics, safety, and passenger comfort.

PLACES VISITED IN SMRC

1. Cockpit Modules by SMRC

The cockpit module is the central interior assembly of a vehicle that integrates several functional components into a single unit. It includes the dashboard structure, steering support, HVAC ducts, glove box, and electronic mounting systems. During the industrial visit, we observed how these modules are manufactured using advanced injection molding and assembly processes. The company emphasized the importance of ergonomics, safety, and lightweight design in cockpit development. These modules help automobile manufacturers reduce assembly time while improving vehicle performance and driver comfort.

2. SMRC Instrument Panels

The instrument panel is an important component that provides drivers with essential vehicle information and controls. It houses the speedometer, fuel gauge, warning indicators, infotainment system, and air vents. During the visit, we learned about the molding, finishing, and assembly processes used to manufacture high-quality instrument panels. Special attention

is given to durability, aesthetics, and safety standards. The panel is designed to provide clear visibility and easy access to controls, enhancing the overall driving experience.



3. SMRC Door Panels

Door panels are interior trim components installed on vehicle doors to improve appearance, comfort, and functionality. They consist of armrests, storage pockets, speaker covers, switch panels, and decorative materials. During the visit, we observed how plastic molding, foam integration, and fabric wrapping techniques are used in their production. The company performs various quality tests to ensure proper fit, durability, and surface finish. Door panels also contribute to noise reduction and passenger safety while enhancing the vehicle's interior aesthetics.

4. SMRC Console Modules

Console modules are located between the driver and passenger seats and serve as a multifunctional control and storage area. These modules include cup holders, storage compartments, gear shift housings, charging ports, and control switches. During the industrial visit, we learned about the design and assembly processes involved in manufacturing these components. The modules are carefully engineered to provide convenience, accessibility, and efficient use of cabin space. They play a significant role in improving the comfort and functionality of modern vehicles.

5. SMRC Decorative Parts

Decorative parts are used to enhance the visual appeal and premium feel of a vehicle's interior. These components include wood-grain trims, metallic finishes, piano-black panels, chrome accents, and other decorative elements. During the visit, the company demonstrated various finishing technologies such as painting, coating, film lamination, and laser etching. These processes ensure attractive appearance, color consistency, and scratch resistance. Decorative parts help manufacturers create unique interior designs and improve customer satisfaction by providing a refined and stylish cabin environment.

Conclusion

The industrial visit to Samvardhana Motherson Reydel Companies (SMRC) was highly informative and educational. It provided practical exposure to the manufacturing of automotive interior systems such as Cockpit Modules, Instrument Panels, Door Panels, Console Modules, and Decorative Parts. Students observed how advanced manufacturing technologies, quality assurance systems, and safety practices are integrated to produce high-quality automotive components. The visit helped bridge the gap between theoretical knowledge and industrial applications, offering valuable insights into modern automobile manufacturing and preparing students for future careers in engineering and industry.

A Report on Industrial Visit to "Centre for Development of Advanced Computing(C-DAC), Bengaluru" Organized by Department of Computer Science & Engineering on 19.05.2026



Report Submitted by: Dr. R. Nidhya, Professor, Department of Computer Science and Engineering
Faculty Members Accompanied: Dr R Nidhya, Professor, Department of Computer Science and Engineering; Mr. T Thangarasan, Assistant Professor, Department of Computer Science and Engineering.
Total no. of Participants: 52
Mode of Conduct: Offline
Report Received on 22.05.2026.

The Department of Computer Science and Engineering organized an industrial visit to the Centre for Development of Advanced Computing (C-DAC), Bengaluru for I B.Tech (CSE) and M.Tech (CSE) students on 19.05.2026. A total of 50 students participated in the visit along with faculty coordinators Dr. R. Nidhya and Mr. T. Thangarasan. The visit was arranged with the objective of providing practical exposure to emerging technologies such as Internet of Things (IoT), Embedded Systems, Drone Technologies, and Cybersecurity.

ABOUT C-DAC:

C-DAC, Bengaluru centre, is the 2nd of C-DAC centres established in 1989 to carry-out research and deliver solutions and products in the area of System Software for PARAM series of supercomputers of C-DAC. PARAM Padma was housed at C-DAC's Terascale Supercomputing Facility (CTSF), results from its third mission project in High-Performance Computing Technologies, which was listed in the Top 500 Supercomputers.

The centre is equipped with PARAM Utkarsh Supercomputing facility, contributing towards National Super computer mission (NSM) and offers HPC System Software solutions & services along with development of debuggers, profilers, web frameworks, high speed data transfer tools for national grid computing, AI framework & big data Software suites, and gateways for multiple scientific application domains.

The centre is highly acclaimed as a centre for excellence in the thematic areas of HPC, Grid Computing, Hardware Security, Cyber Security, IoT, DNSSec, Strategic Electronics, FOSS and Software Technologies, Language and Heritage Computing, Knowledge & Resource Centre for Accessibility, ICT for Social welfare, Consultancy and Training. C-DAC Bengaluru is enriched with indigenous solutions in cloud security, next generation SCADA systems, Industrial control systems, critical infrastructure Security, power optimization for HPC, Network firewall systems, Indian Heritage and Language computing, IoT solutions for smart cities, Smart Post Kiosk, Indus IoT Kit, IoT Lab Kit, Indus Copter, Smart Energy Meter, ICT for Social Welfare, 32-bit High performance Microcontroller, chip design, crypto module validation and Quantum Simulator. This center is involved in disseminating vast experience for the enrichment of education and knowledge.





About the Visit:

During the technical session, Mr. M. Rohith Reddy, Project Engineer, delivered an informative presentation on IoT technologies and embedded development platforms used at C-DAC. He explained the architecture and applications of IoT chips and development boards such as BLEmote and WiFiMote, highlighting their role in smart communication and connected systems. The session also covered real-time IoT applications, wireless communication protocols, sensor integration, and smart device connectivity. In addition, he demonstrated drone development projects carried out at C-DAC and explained the integration of IoT, embedded systems, and wireless technologies in drone communication and automation systems. He also explained in detailed about PARAM supercomputer to the students.

Another session was handled by Ms. Akshaya, Project Associate, who educated students about various cyberattacks, security threats, and preventive mechanisms in modern digital systems. The session focused on cybersecurity awareness, ethical hacking concepts, phishing attacks, malware, ransomware, network vulnerabilities, and best practices for cyber protection. Students gained practical knowledge regarding cyber defence strategies and secure digital communication systems.

The industrial visit provided students with valuable exposure to advanced technologies and current industrial practices. The interaction with C-DAC experts enhanced the students' understanding of IoT systems, drone technologies, embedded applications, and cybersecurity concepts. The visit also motivated students to explore research and career opportunities in emerging technology domains. Overall, the industrial visit was highly informative, interactive, and beneficial in bridging the gap between academic learning and industrial applications.



Outcomes of Industrial Visit:

- Gained practical exposure to IoT technologies and smart connected systems.
- Learned the working of BLEmote, WiFiMote, and embedded IoT devices.
- Understood drone development and real-time industrial applications at C-DAC.
- Enhanced knowledge about cybersecurity threats, cyber attacks, and prevention techniques.
- Improved understanding of wireless communication, sensors, and smart systems integration.
- Interacted with industry experts and gained awareness about emerging technology trends.
- Bridged the gap between theoretical concepts and industrial applications in IoT and cybersecurity.

Newspaper Clips:

సాక్షి

సీ–డాక్ సందర్శనలో విద్యార్థులు



బెంగళూరులోని సీడాక్ సందర్శనలో మిట్స్ విద్యార్థులు

కురబలకోట: సాంకేతిక విద్యలో ప్రాక్టికల్ పరిజ్ఞానం పెంపొందించేందుకు బెంగళూరులోని కేంద్ర పరిశోధనా సంస్థ సెంటర్ ఫర్ డెవలప్‌మెంట్ ఆఫ్ అడ్వాన్స్‌డ్ కంప్యూటింగ్ (సీ–డాక్) కేంద్రాన్ని అంగళ్లులోని మిట్స్ డీమ్డ్ యూనివర్సిటీ మొదటి సంవత్సరం విద్యార్థులు మంగళవారం సందర్శించినట్లు వీసీ యువరాజ్ తెలిపారు. అత్యాధునిక సాంకేతిక పరిశోధనలు, వినూత్న ప్రాజెక్టులను ప్రత్యక్షంగా పరిశీలించి సాంకేతిక ప్రపంచంలోని తాజా పరిణామాలను తెలుసుకున్నారన్నారు. తరగతి గదిలో నేర్చుకున్న సిద్ధాంత పరిజ్ఞానాన్ని పరిశ్రమల్లో అమలవుతున్న ప్రాక్టికల్ సాంకేతికతతో అనుసంధానించుకునే అరుదైన అవకాశం లభించిందని ఈ సందర్భంగా విద్యార్థులు ఆనందం వ్యక్తం చేశారు.

20/05/2026 | Madanapalle | Page : 9
Source : https://epaper.sakshi.com/

పబ్లిక్ టుడే

కడప / అన్నమయ్య బుధవారం 20 మే 2026

మిట్స్ యూనివర్సిటీ విద్యార్థులకు సీతారాం జిందాల్ స్కాలర్‌షిప్‌లు



మిట్స్ యూనివర్సిటీ విద్యార్థుల కోసం నిర్వహించిన..

విద్యా పర్యటన కార్యక్రమం..





తరంగం

20/05/2026 | Page No: 5

మిట్స్ వర్సిటీ విద్యార్థులు సీ-డాక్ కేంద్రం సందర్శన





సూర్య

మిట్స్ విద్యార్థులచే పారిశ్రామిక విద్యాపర్యటన



ఆంధ్రప్రభ



SKYLINE

ENGLISH DAILY

VOLUME : 21, NO. 139 TIRUPATI, WEDNESDAY, MAY 20, 2026 PAGES 8 Rs. 1/-

MITS students visit the C-DAC centre in Bangalore as part of an industrial education tour



4 పబ్లిక్ టుడే

పారిశ్రామిక విద్యా పర్యటన

విద్యార్థులకు అవగాహన



మిట్స్ వర్సిటీ విద్యార్థులు సీ-డాక్ సందర్శన

- గ్లోబల్ టెక్నాలజీపై ఉపాధి అవగాహన : విసి

A Report on Industrial Visit to "Cimtrix Systems Pvt. Ltd" Organized by Department of Computer Science & Engineering on 21.05.2026



Report Submitted by: Dr. K Senthil Kumar, Assistant Professor, Department of Computer Science & Engineering.
Faculties Accompanied: Dr. K Senthil Kumar, R Kavi Priya from the Department of Computer Science & Engineering
Mode of Conduct: Offline
Participants: 39 Students from I CSE E
Report Received on 23.05.2026.

Introduction

A One-day Industrial Visit to **Cimtrix Systems Pvt. Ltd., Bengaluru** was organized for the students of the Department of Computer Science and Engineering, I B.Tech CSE – E Section. The main purpose of the visit was to provide practical exposure to modern technologies such as **3D Printing, 3D Scanning, Product Design, and Industrial Applications of Advanced Manufacturing Technologies**.
Cimtrix Systems Pvt. Ltd. is a technology-based company specializing in innovative engineering solutions, 3D Printing technologies, 3D Scanning solutions, Product Development, Reverse Engineering, and Digital Manufacturing applications. The visit provided students with an opportunity to understand real-time industrial practices and the latest technological advancements used in manufacturing and design industries.

Objectives of the Visit

- To provide practical exposure to 3D Printing and 3D Scanning technologies.
- To understand the industrial applications of additive manufacturing.
- To bridge the gap between academic learning and industrial practices.
- To improve awareness about emerging technologies and innovation.
- To enhance students' technical knowledge and professional skills.
- To provide insights into product design and prototyping techniques.

About Cimtrix Systems Pvt. Ltd.

Cimtrix Systems Pvt. Ltd., Bengaluru is a leading technology company working in the areas of 3D Printing, 3D Scanning, Reverse Engineering, CAD/CAM Solutions, and Product Development. The company provides advanced engineering solutions for industries such as manufacturing, automotive, aerospace, healthcare, and education.
The organization focuses on innovation and practical implementation of modern engineering technologies. Through technical demonstrations and industrial interactions, the company helps students understand real-time applications of additive manufacturing and digital design technologies.

Technical Session on 3D Printing and 3D Scanning

A technical session was conducted for the students by **Mr. Yathish, HR, Cimtrix Systems Pvt. Ltd.** He explained the importance of 3D Printing and 3D Scanning technologies in modern industries and their applications in product development and manufacturing processes.

The session focused on:

- Fundamentals of 3D Printing technology.
- Working principles of additive manufacturing.
- Types of 3D Printers and printing materials.
- Industrial applications of 3D Printing.
- Basics of 3D Scanning and Reverse Engineering.
- Product prototyping and digital manufacturing techniques.

Students actively participated in the session and clarified their doubts through interaction with the industry experts.



Figure 1Introduction to reverse engineering

Exposure to 3D Printing Technology

Students were introduced to different types of 3D printers and their industrial applications.
The experts demonstrated:

- Layer-by-layer printing process.
- Design conversion from digital models to physical objects.
- Usage of printing materials such as PLA and ABS.
- Rapid prototyping techniques used in industries.
- Applications of 3D Printing in automotive, medical, and manufacturing sectors.

Students gained practical knowledge about how 3D models are designed, processed, and converted into real products using additive manufacturing technology.



Figure 2: Introduction to different 3d printing materials

Demonstration on 3D Scanning and Reverse Engineering

The technical team provided a demonstration on 3D Scanning technologies and Reverse Engineering concepts.
Students observed:

- Working of 3D Scanners.
- Digital capturing of object dimensions and shapes.
- Conversion of physical objects into digital models.
- Applications of Reverse Engineering in industries.
- Accuracy and precision involved in industrial scanning processes.

The demonstration helped students understand the practical implementation of scanning technologies in product development and quality analysis.



Figure 3: Introduction to 3d scanning

Industry Interaction and Career Guidance

An interactive session was conducted for the students regarding current industry trends and career opportunities in emerging technologies.

The experts provided guidance on:

- Career opportunities in 3D Printing and Product Design.
- Skills required for industrial and software domains.
- Importance of innovation and practical learning.
- Internship and certification opportunities.
- Emerging trends in digital manufacturing technologies.

Students actively interacted with the experts and gained valuable industry insights.



Figure 4: Introduction to software used for 3d printing

Conclusion

The Industrial Visit to **Cimtrix Systems Pvt. Ltd., Bengaluru** was highly informative and beneficial for the students of I B.Tech CSE – E Section. The visit provided practical exposure to 3D Printing, 3D Scanning, Reverse Engineering, and Product Development technologies.

Students gained valuable knowledge through technical sessions, demonstrations, and interaction with industry experts. The visit helped students understand the practical applications of concepts learned in academics and improved their awareness about modern industrial technologies and career opportunities.

Overall, the Industrial Visit successfully enhanced the technical knowledge, practical understanding, and professional awareness of the students.

Acknowledgement

We express our sincere gratitude to the Management, Principal, Vice Principal Administration, Head of the Department of Computer Science and Engineering for providing support and permission to organize this industrial visit.

We also thank **Mr. Yathish, HR, Cimtrix Systems Pvt. Ltd.**, and the entire technical team for their valuable guidance, support, and hospitality during the visit.

A Report on Industrial Visit to "Centum Electronic Limited, Bengaluru" Organized by Department of Computer Science & Engineering on 22.05.2026





Report Submitted by: Dr. G. Arun Kumar, Associate Professor, Department of Computer Science & Engineering
Faculty Members Visited: Dr. G. Arun Kumar, Associate Professor, Department of Computer Science & Engineering; Mrs. G. Vasundara Devi, Assistant Professor, Department of Computer Science & Engineering.
Total no. of Participants: 42
Mode of Conduct: Offline
Report Received on 01.06.2026.

The Department of Computer Science and Engineering organized an industrial visit to the Centum Electronic Limited, Bengaluru for I B.Tech (CSE). A total of 40 students participated in the visit along with faculty coordinators Dr. G. Arun Kumar and Mrs. G. Vasundhara Devi The industrial visit to Centum Electronics Limited, Bengaluru, aims to provide students with practical exposure to advanced electronics manufacturing, aerospace and defence technologies, and industry-standard practices. The visit helps bridge the gap between academic learning and real-world applications by offering insights into product design, system integration, quality assurance, and innovation in high-reliability electronic systems.

About Centum Electronic Limited:

Centum Electronics Limited is a leading Indian Electronics System Design and Manufacturing (ESDM) company headquartered in Bengaluru. Founded in 1993–94, the company specializes in designing, developing, and manufacturing high-reliability electronic systems, subsystems, and components for aerospace, defense, space, industrial, medical, and communications sectors. It serves major organizations such as ISRO, DRDO, defense PSUs, and global aerospace companies. Centum has manufacturing facilities in India and international operations across Europe and North America. The company is recognized for its expertise in mission-critical electronics, avionics, power systems, radar, electronic warfare, and satellite applications.

Highlights of Products Built by Centum Electronics Limited

- **Aerospace & Space Electronics –** Designs and manufactures satellite electronics, onboard power systems, telemetry systems, and mission-critical modules used in space missions.
- **Defense Systems –** Develops electronic warfare systems, radar subsystems, communication systems, missile electronics, and battlefield support equipment.
- **Avionics Solutions –** Produces flight-control electronics, cockpit display systems, navigation modules, and aircraft communication equipment.
- **Power Electronics –** Manufactures high-reliability power supplies, power distribution units, battery management systems, and power converters for defense and industrial applications.
- **Radar & RF Systems –** Builds RF and microwave modules, radar transmit/receive units, signal processing systems, and antenna-related electronics.
- **Industrial Electronics –** Provides embedded systems, control units, and custom electronic assemblies for industrial automation and monitoring.
- **Medical Electronics –** Develops high-precision electronic assemblies and subsystems for healthcare and diagnostic equipment.
- **Electronics Manufacturing Services (EMS) –** Offers end-to-end PCB assembly, system integration, testing, prototyping, and product lifecycle management services for global customers.

About the Visit:

During the industrial visit to Centum Electronics Limited, Bengaluru, students were given an opportunity to observe the complete electronics manufacturing process followed in the company. Mr. Lakshmi Narayanan, Production Engineer, provided a detailed explanation of the production workflow, quality standards, and advanced technologies used in the organization.

He explained how electronic products for aerospace, defense, space, and industrial applications are designed, assembled, tested, and delivered to customers. The students gained valuable insights into the importance of precision, reliability, and quality control in the manufacturing of mission-critical electronic systems.



Key Highlights of the Visit

1. Introduction to the Electronics Manufacturing Process and Production Facilities.
2. Overview of Printed Circuit Board (PCB) Assembly using Surface Mount Technology (SMT).
3. Demonstration of Automated Pick-and-Place Machines for component placement.
4. Explanation of Solder Paste Printing and Reflow Soldering techniques.
5. Understanding of Through-Hole Component Assembly and Manual Soldering Processes.
6. Exposure to Quality Assurance Procedures and Inspection Standards.
7. Demonstration of Automated Optical Inspection (AOI) Systems for defect detection.
8. Understanding of Functional Testing and Reliability Testing of electronic products.
9. Insights into Aerospace and Defense Electronics Manufacturing Requirements.
10. Importance of Documentation, Traceability, and Process Control in production.
11. Discussion on Industry 4.0 practices and automation in modern manufacturing.
12. Interaction session on career opportunities and skill requirements in the electronics industry.

The visit provided practical exposure to industrial manufacturing practices and helped students understand how theoretical concepts learned in classrooms are applied in real-world electronics production environments. The interaction with Mr. Lakshmi Narayanan enabled students to gain valuable knowledge about production engineering, quality management, and advanced electronic system manufacturing.

Outcomes of Industrial Visit:

1. Gained practical exposure to advanced electronics manufacturing processes used in aerospace, defense, and industrial applications.
2. Developed an understanding of PCB assembly techniques, Surface Mount Technology (SMT), and automated production systems.
3. Learned about quality assurance, testing procedures, and industry standards followed in mission-critical electronic product development.
4. Acquired knowledge of modern manufacturing technologies, including automated inspection and process control systems.
5. Understood the importance of precision, reliability, documentation, and traceability in electronics production.
6. Enhanced awareness of career opportunities, technical skills, and industry expectations in the electronics and embedded systems sectors.
7. Bridged the gap between theoretical concepts studied in engineering courses and their practical implementation in a real industrial environment.

Special Thanks

We express our sincere gratitude to Mr. M. Vijay, HR Manager, Centum Electronics Limited, Bengaluru, for organizing and facilitating a smooth and informative industrial visit. His warm hospitality, excellent coordination, and continuous support throughout the visit made the experience highly valuable and memorable for all participants.
We would also like to extend our heartfelt thanks to him and the entire Centum Electronics team for providing a wonderful lunch and ensuring the comfort and well-being of all students and faculty members. Their generous support, guidance, and hospitality greatly contributed to the success of this industrial visit.

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE

(UGC-AUTONOMOUS INSTITUTION)
Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi
NAAC Accredited with A+ Grade, NIRF India Rankings 2025 - Band: 201-300 (Engg.)
NBA Accredited - B.Tech. (CIVIL, CSE, CST, ECE, EEE, MECH), MBA & MCA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

List of MoU's Signed

AY: 2025 - 2026				
S.No.	**Name of Industry / Institute / Organization**	**Location**	**Title / Purpose**	**Date**
1	Bellu AI	Madanapalle, Andrapradesh, India	Training/Research	22.12.2025
2	RAMPeX	Sathyamangalam, Erode District, Tamil Nadu, India	Internship	21.01.2026
3	ROOMAN	Electronic City, Bengaluru – 560100, Karnataka, India	Training/Placement	20.02.2026

HoD/CSE

Head of the Department
Computer Science & Engineering
Madanapalle Institute of Technology & Science
MADANAPALLE-517 325.

MEMORANDUM OF UNDERSTANDING(MOU)

BETWEEN

Department of Computer Science & Engineering

Madanapalle Institute of Technology & Science (MITS) Deemed to be University

&

Bellu AI, Madanapalle

MEMORANDUM OF UNDERSTANDING

This **Memorandum of Understanding** (hereinafter called as the 'MOU') is entered into on this the 22 – DECEMBER – Two Thousand Twenty Five (22.12.2025).

BETWEEN

Department of Computer Science & Engineering, Madanapalle Institute of Technology & Science (MITS) Deemed to be University, the First Party Kadiri Road Angallu, Village, Madanapalle, Chittoor District represented by its **Professor & Head/CSE Dr M Sreedevi** (hereinafter referred as **'First Party'**, the institution which expression, unless excluded by or repugnant to the subject or context shall include its successors – in-office, administrators and assigns).

AND

Bellu AI, @BK Palle, Madanapalle, **the Second Party,** represented by its **CEO Mr. Bukya Santosh Naik** (hereinafter referred to as **'Second Party'**, company which expression, unless excluded by or repugnant to the subject or context shall include its successors –in-office, administrators and assigns).

(First Party and Second Party are hereinafter jointly referred to as 'Parties' and individually as 'Party')

WHEREAS:

A) First Party is a Higher Educational Institution named:

i. **Head of the Department, Department of Computer Science & Engineering, Madanapalle Institute of Technology & Science Deemed to be University**

B) First Party & Second Party believe that collaboration and co-operation between themselves will promote more effective use of each of their resources and provide each of them with enhanced opportunities.

C) The Parties intent to cooperate and focus their efforts on cooperation within area of Skill Based Training, Education, Placement, Industrial Visit, Expert Lecture.

D) Both Parties, being legal entities in themselves desire to sign this MOU for advancing their mutual interest.

E) **Bellu AI**, the Second Party is engaged in Product Development, Skill Development and R&D Services in the fields of Information Technology.

F) **Bellu AI, Madanapalle**, the Second Party is promoted by (group/promoter) first party.

G) **Bellu AI, Madanapalle**, a Software Development, Digital Marketing company offering various services to the Colleges and Corporate such as Value Added Training and Project Development.

NOW THEREFORE, IN CONSIDERATION OF THE MUTUAL PROMISES SET FORTH IN THIS MOU, THE PARTIES HERETO AGREE AS FOLLOWS:

CLAUSE 1 CO-OPERATION

1.1 Both Parties are united by common interests and objectives, and they shall establish channels of communication and co-operation that will promote and advance their respective operations within the **Institution** and its related wings. The Parties shall keep each other informed of potential opportunities and shall share all information that may be relevant to secure additional opportunities for one another.

1.2 First Party and Second Party co-operation will facilitate effective utilization of the intellectual capabilities of the faculty of First Party providing significant inputs to them in developing suitable teaching / training systems, keeping in mind the needs of the industry, the Second Party.

1.3 The general terms of co-operation shall be governed by this MOU. The Parties shall cooperate with each other and shall, as promptly as is reasonably practical, enter into all relevant agreements, deeds and documents (the 'Definitive Documents') as may be required to give effect to the actions contemplated in terms of this MOU. The term of Definitive Documents shall be mutually decided between the Parties. Along with the Definitive Documents, this MOU shall represent the entire understanding as to the subject matter hereof and shall

supersede any prior understanding between the Parties on the subject matter hereof.

CLAUSE 2 SCOPE OF THE MoU

2.1 The budding graduates from the institutions could play a key role in technological up-gradation, innovation and competitiveness of an industry. Both parties believe that close co-operation between the two would be of major benefit to the student community to enhance their skills and knowledge.

2.2 **Curriculum Design:** Second Party will give valuable inputs to the First Party in teaching / training methodology and suitably customize the curriculum so that the students fit into the industrial scenario meaningfully.

2.3 **Industrial Training & Visits:** Industry and Institution interaction will give an insight into the latest developments / requirements of the industries; the Second Party to permit the Faculty and Students of the First Party to visit its group companies and also involve in Industrial Training Programs for the First Party. The industrial training and exposure provided to students and faculty through this association will build confidence and prepare the students to have a smooth transition from academic to working career. The Second Party will provide its Labs / Workshops / Industrial Sites for the hands-on training of the learners enrolled with the First Party.

2.4 **Internships and Placement of Students:** Second Party will actively engage to help the delivery of the Internship and placement of students of the First Party into internships/jobs, as per AICTE internship Policy. The Second Party will also register itself on AICTE Internship Policy Portal for disseminating the Internship opportunities available with them.

2.5 **Research and Development:**

a. Both Parties have agreed to carry out the joint research activities in the emerging fields of **Computer Science & Engineering.**

b. While publishing data which is collected from college its needs to be explicitly mentioned that data has been collected in collaboration with MITS Deemed to be University.

c. It needs to be mandated that "Anyone using the data needs to acknowledge that data used has been collected in in collaboration with MITS Deemed to be University".

d. Privacy of the student participating in data collection should be guaranteed.

2.6 **Skill Development Programs:** Second Party to train the students of First Party on the emerging technologies in order to bridge the skill gap and make them industry ready.

2.7 **Guest Lectures:** Second Party to extend the necessary support to deliver guest lectures to the students of the First Party on the technology trends and in house requirements.

2.8 **Faculty Development Programs:** Second Party to train the Faculties of First Party for imparting industrial exposure/ training as per the industrial requirement considering the National Occupational Standards in concerned sector, if available.

2.9 Both Parties to obtain all internal approvals, consents, permissions, and licenses of whatsoever nature required for offering the Programs on the terms specified herein.

2.10 There is no financial commitment on the part of the **Head of the Department, Department of Computer Science & Engineering, Madanapalle Institute of Technology & Science**, the First Party to take up any program mentioned in the MOU. If there is any financial consideration, it will be dealt separately.

CLAUSE 3 INTELLECTUAL PROPERTY

3.1 Nothing contained in this MOU shall, by express grant, implication, Estoppel or otherwise, create in either Party any right, title, interest, or license in or to the intellectual property (including but not limited to know-how, inventions, patents,

copyrights and designs) of the other Party.

CLAUSE 4 VALIDITY

4.1 This Agreement will be valid until it is expressly terminated by either Party on mutually agreed terms, during three year of period (22.12.2025 to 31.12.2027) **Bellu AI**, the Second Party, as the case may be, will take effective steps for implementation of this MOU. Any act on the part **Bellu AI**, the Second Party after termination of this Agreement by way of communication, correspondence etc., shall not be construed as an extension of this MOU

4.2 Both Parties may terminate this MOU upon 30 calendar days' notice in writing. In the event of Termination, both parties have to discharge their obligations.

CLAUSE 5 RELATIONSHIP BETWEEN THE PARTIES

5.1 It is expressly agreed that **First Party** and **Second Party** are acting under this MOU as independent contractors, and the relationship established under this MOU shall not be construed as a partnership. Neither Party is authorized to use the other Party's name in any way, to make any representations or create any obligation or liability, expressed or implied, on behalf of the other Party, without the prior written consent of the other Party. Neither Party shall have, nor represent itself as having, any authority under the terms of this MOU to make agreements of any kind in the name of or binding upon the other Party, to pledge the other Party's credit, or to extend credit on behalf of the other Party.

FirstParty SecondParty

Any divergence or difference derived from the interpretation or application of the MoU shall be resolved by arbitration between the parties as per the Arbitration Act, 1996. The place of the arbitration shall be at District Head Quarters of the First Party. This undertaking is to be construed in accordance with Indian Law with exclusive jurisdiction in the Courts of Madanapalle.

AGREED:

For **Head of the Department,**
Department of Computer Science & Engineering
Madanapalle Institute of Technology & Science

Authorized Signatory

For **Bellu AI**

Authorized Signatory

Name of Institution	Name of Industry
Head of the Department, Department of Computer Science & Engineering Madanapalle Institute of Technology & Science, the First Party Kadiri Road Angallu, Village, Madanapalle, Madanapalle District	**Bellu AI** Mr. Bukya Santosh Naik BK Palle, Madanapalle Madanapalle, Madanapalle District.
Contact Details: Dr M Sreedevi	Contact Details: 6309583248
E-mail: csehod@mits.com	E-mail: santhosh@bellu.ai
Web : www.mits.ac.in	Web: www.bellu.ai

Witness 1:

Dr. C.P. Gupta
Dean/SoC

Dr. R. Nidhya
Prof/ CSE

Witness 2:

Mr. Sai Ganesh. K
Member in Bellu AI

Witness 3:

Mr. Reddy Pavan
Member in Bellu AI

Witness 4:

MITS University/ IRC/DL/MOU/26/01/014-CSE

MITS
MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(Deemed to be University under Section 3 of UGC Act, 1956)

RAMPeX
Reboot your learning experience

MEMORANDUM OF UNDERSTANDING (MoU)

Between
MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE (Deemed to be) UNIVERSITY
Madanapalle, Andhra Pradesh, India
(A Deemed-to-be University offering Engineering, Management, MCA, and M. Tech Programmes)
and
RAMPEX TECHNOLOGIES SFNO: 90/2, KARATOOR ROAD, RANGASAMUDRAM, Sathyamangalam, Erode, Tamil Nadu, 638402.

1. PREAMBLE

This Memorandum of Understanding (MoU) is entered into on the date of signature below between **Madanapalle Institute of Technology & Science (*deemed-to-be) University*, Madanapalle, Andhra Pradesh**, a premier academic institution offering Engineering (BE), Management (MBA), Master of Computer Applications (MCA), and Master of Technology (M.Tech) programmes with research and consultancy mandates, and **RAMPEX TECHNOLOGIES** , an industrial training venture organization based in Location, State , Engaged in providing training services in the Engineering and Management sectors as a training partner.

2. PURPOSE OF THE MoU

The purpose of this MoU is to establish a formal framework between MITS & RAMPEX TECHNOLOGIES to:

1. Facilitate structured internships, apprenticeships, and project-based learning for MITS students aligned to RAMPEX TECHNOLOGIES 's operational and strategic needs
2. Enable faculty engagement in consultancy, applied research, process improvement, and product innovation
3. Support RAMPEX TECHNOLOGIES 's digital transformation, quality improvement, sustainability, and export-readiness objectives
4. Facilitate regular knowledge-sharing, expert talks, workshops, and industry–academia networking
5. Establish a pathway for ongoing, mutually beneficial collaboration in emerging technologies (Development, Cloud, DevOps, Programming and Problem Solving using DSA)

3. SCOPE & KEY COLLABORATION AREAS

3.1 Student Internships & Project-Based Learning

- **Internships:** MITS shall nominate qualified students for semester-long or project-based internships at RAMPEX TECHNOLOGIES facilities.
    - Duration: Typically, 04 to 06 weeks for 3rd year Students and 04 to 06 months for Final year Students, aligned to MITS academic calendar.

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- Student selection: Based on academic merit, skill fit, and RAMPEX TECHNOLOGIES specific requirements.
- Supervision: Dual mentorship by MITS faculty and RAMPEX TECHNOLOGIES technical lead.

- **Capstone & Industry-Guided Projects:** Student teams (UG, PG, MBA, M. Tech) undertake live projects addressing RAMPEX TECHNOLOGIES challenges in the areas of Process optimization, product design, digital systems, quality improvement, supply chain, sustainability
  - Deliverables: Feasibility reports, prototypes, software/simulation tools, recommendations.
  - Ownership: Academic work retained by students/MITS; RAMPEX TECHNOLOGIES gains first right to implement findings
- **Plant Visits & Exposure:** Organized facility tours and shopfloor orientation for MITS students to understand RAMPEX TECHNOLOGIES operations, technologies, and industry best practices.

3.2 Faculty Consultancy & Research Collaboration

- **Consultancy Projects:** MITS faculty engage with RAMPEX TECHNOLOGIES on:
  - Process optimization and efficiency improvement
  - Product innovation and design iteration
  - Quality systems, automation, Industry 4.0 adoption
  - Sustainability and circular economic initiatives
- **Research Partnerships:** Joint research proposals in emerging areas:
  - RAMPEX TECHNOLOGIES identifies R&D challenges; MITS proposes solutions.
  - Joint funding through government schemes (DST, DBT, IMPRINT, ICMR, etc.) or RAMPEX TECHNOLOGIES sponsorship.
  - Co-authored publications (subject to confidentiality agreements).
- **Faculty Sabbaticals:** MITS may nominate 1–2 faculty annually for 4 to 8 weeks industry immersions at RAMPEX TECHNOLOGIES for:
  - Technology capability assessment
  - Curriculum enhancement insights
  - Industry problem-solving participation

3.3 Skill Development Programme & Workforce Upskilling

- **Customized Training Programmes:** Co-design short-term, industry-specific courses (40–120 hours) if applicable for in the areas of EV battery systems, advanced manufacturing, quality management, digital systems, sustainability, supply chain:
  - RAMPEX TECHNOLOGIES employees (upskilling, cross-skilling)
  - MITS students (pre-placement / capability building)
  - Open-ended delivery as mutually agreed.
- **Certification & Credentials:** Programs shall carry RAMPEX TECHNOLOGIES 's endorsement and industry-recognized certifications (e.g., ISO, SIAM, FSAI, etc.) to enhance participant employability.

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- **Train-the-Trainer:** MITS develops trainer capacity within RAMPEX TECHNOLOGIES for sustainable, independent skill delivery post-MoU.

3.4 Placement & Recruitment Interface

- **Direct Placement Pathway:** RAMPEX TECHNOLOGIES considering evaluating MITS final-year students for direct placement/recruitment. MITS shall coordinate campus interviews, presentations, and candidate profiles.
- **Lateral Hiring & Internship-to-Job:** High-performing interns may be offered internship-extension or direct employment as per RAMPEX TECHNOLOGIES policy and MITS recommendations.

3.5 Innovation & Technology Partnership

- **Problem-Solving Collaboration:** MITS research centers, labs, and maker spaces available to RAMPEX TECHNOLOGIES for:
  - Prototype development and testing
  - CAD design and simulation
  - Material testing and quality analysis
  - Digital solution development (software, IoT, AI/ML)
- **Industry 4.0 & Digital Transformation:** MITS faculty assist RAMPEX TECHNOLOGIES in:
  - Automation readiness assessment
  - Digital process mapping
  - IoT / data analytics implementation consulting

3.6 Thought Leadership & Knowledge Dissemination

- **Expert Talks & Webinars:** RAMPEX TECHNOLOGIES executives deliver monthly/quarterly guest lectures at MITS on industry trends, case studies, and leadership insights.
- **Workshop Series:** Quarterly workshops (joint design) on sector-specific topics (e.g., "Advanced Manufacturing," "EV Systems," "Food Safety Compliance") open to MITS students and faculty.
- **Case Studies & Documentation:** Success stories from collaboration documented for:
  - MITS placement cell and student communication
  - RAMPEX TECHNOLOGIES marketing and CSR visibility
  - Academic publications (with confidentiality protections)

4. ROLES & RESPONSIBILITIES

4.1 MITS Shall

1. **Student Nomination & Coordination:** Identify, nominate, and monitor MITS students for internships, projects, and visits. Ensure academic standards, attendance, and learning outcome documentation.
2. **Faculty Engagement:** Nominate qualified faculty for consultancy, research, and teaching assignments. Coordinate their availability and ensure knowledge transfer back to curricula.
3. **Infrastructure & Labs:** Provide access to laboratories, testing facilities, CAD suites, and workshops for:

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- Student project execution
- Faculty consultancy and prototyping

4. **Quality Assurance:** Maintain academic rigor, ensure student conduct and safety compliance, and collect feedback from **RAMPEX TECHNOLOGIES** on student and faculty performance.
5. **Documentation & Reporting:** Maintain records of internships, projects, consultancy outcomes, and participant feedback. Provide **quarterly progress reports** to RAMPEX TECHNOLOGIES designated contact.

4.2 RAMPEX TECHNOLOGIES Shall

1. **Internship & Project Hosting:** Identify internship positions and live project opportunities for MITS students. assign mentors, provide facilities, and document learning outcomes.
2. **Student & Faculty Feedback:** Provide structured feedback on student/faculty performance, identifying strengths and areas for improvement. Share this with MITS for curriculum enhancement.
3. **Recruitment & Placement:** Participate in MITS campus recruitment drives (as per annual agreement). Interviewing final-year students and extend placement offers competitive talent.
4. **Guest Lectures & Workshops:** Nominate industry experts to deliver monthly guest talks and co-design quarterly workshops for MITS students and faculty.
5. **Knowledge Sharing:** Share (within confidentiality limits) process descriptions, technical specifications, industry challenges, and best practices to inform MITS curriculum and research focus.
6. **Coordination & Communication:** Designate a **Single Point of Contact (SPOC)** from RAMPEX TECHNOLOGIES 's HR/Operations/Innovation team to manage all coordination with MITS.
7. **Commitment Letters:** Provide annual **Letter of Intent (LOI)** specifying:
   - Number of internship positions and project opportunities
   - Preferred internship duration and timing (aligned to MITS semesters)
   - Skill/discipline focus (e.g., Mechanical, Electrical, Manufacturing, Software, etc.)
   - Placement targets and recruitment timeline
   - Training Programme participation (if any)

5. GOVERNANCE & REVIEW MECHANISM

5.1 Collaboration Oversight (Coordination, Not Hierarchical)

A **Collaboration Coordination Group (CCG)** shall be constituted to oversee implementation and ensure mutual satisfaction:

MITS Representatives:

- Vice Chancellor/ Assistant Director, Industry Relations / Dean/ HoD
- One Faculty Nominee (rotating annually by department)
- Dean, Academic Affairs (optional, for curriculum matters)

RAMPEX TECHNOLOGIES Representatives:

- Single Point of Contact (SPOC) – HR / Operations / Innovation Lead
- Department Head or Manager (where students are placed)
- (Optional) Sustainability or R&D Lead (if innovation projects are active)

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5.2 Joint Meetings & Functions

- **Frequency: Bi-annual** (June and December) + ad-hoc meetings as needed (virtual or in-person).
- **Venue:** Alternating between **MITS University** and **RAMPEX TECHNOLOGIES** facilities (or virtual).
- **Key Agenda Items:**
  - Track internship placements, student feedback, and **RAMPEX TECHNOLOGIES** satisfaction
  - Review project completions and deliverables
  - Monitor skill Programme participation and outcomes
  - Plan next-cycle placements, visits, and training initiatives
  - Address operational challenges, grievances, and bottlenecks
  - Update annual commitments and Letter of Intent for subsequent year

6. DURATION & RENEWAL

1. **Initial Term:** This MoU shall be valid for **two (2) years** from the date of execution.
2. **Automatic Renewal:** Upon expiry, the MoU shall automatically renew for successive **two-year periods**, unless either party provides written notice of non-renewal **at least 60 days** prior to expiry.
3. **Amendment:** Any modifications to the scope, terms, or obligations may be made by mutual written consent, documented via an amendment addendum signed by both parties.
4. **Continuation of Ongoing Activities:**
   - Any student internships in progress at the time of termination shall **continue to be completed**.
   - Ongoing projects shall be concluded or handed over as mutually agreed.
   - Final documentation and settlement shall be completed within 60 days of termination notice.

7. FINANCIAL ARRANGEMENTS

1. **No Monetary Exchange Between Institution & RAMPEX TECHNOLOGIES:** This MoU does not involve financial payments between MITS and RAMPEX TECHNOLOGIES. Each party bears its own operational costs.
2. **Internship & Project Engagement:**
   - **RAMPEX TECHNOLOGIES may provide:** Stipend to interns (optional, as per RAMPEX TECHNOLOGIES policy), facility access, materials for projects.
   - **MITS may provide:** Student working, faculty expertise, laboratory facilities (at concessional/no cost as a partnership contribution).
3. **Consultancy Projects:**
   - Consulting rates (if applicable to faculty R&D) shall be agreed in a **separate Consulting Agreement** for each project, typically at 20–40% below market rate as a partnership incentive.
4. **Jointly Funded R&D:**
   - Both parties may jointly apply for government grants (DST, DBT, IMPRINT, ICMR, etc.).
   - Cost-sharing and funding allocation to be documented in project-specific MOUs.

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Madanapalle Institute of Technology & Science
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RAMPEX TECHNOLOGIES PVT. LTD. ERODE

8. CONFIDENTIALITY & INTELLECTUAL PROPERTY

8.1 Confidential Information (Mutual Protection)

Both parties may share sensitive business, technical, or process information during collaboration. Such information shall:

- **Be used solely** for purposes outlined in this MoU
- **Be protected from unauthorized disclosure** by both parties and their representatives
- **Be clearly marked** as "Confidential" when shared (email, document header, or verbal acknowledgment)
- **Be returned or destroyed upon request** or termination of MoU
- **Not be disclosed to third parties** without prior written consent of the originating party

Exception: Information required by law (Right to Information Act, government directive, court order) to be disclosed; the disclosing party shall provide **72 hours' notice** to the originating party to enable assertion of confidentiality protections.

8.2 Student & Participant Data Protection

- Student internship records, feedback, and performance assessments shall be maintained by MITS data governance policies.
- RAMPEX TECHNOLOGIES shall not disclose MITS student information without explicit student consent.
- Similarly, MITS shall not disclose RAMPEX TECHNOLOGIES proprietary or confidential information without consent.

9. TERMINATION & DISPUTE RESOLUTION

9.1 Termination Provisions

1. **Mutual Termination:** Both parties may agree to terminate by mutual written consent, with clear documentation of reasons and transition plan.
2. **Unilateral Termination:** Either party may terminate by providing **90 days advance written notice** if:
   - Material breach of MoU occurs and is not remedied within 30 days of written notice
   - Unforeseen circumstances (force majeure, business discontinuation etc.)
3. **Automatic Termination:** This MoU shall automatically expire at the end of each renewal period unless both parties affirm continuation via a renewal letter.

9.2 Continuation of Ongoing Work

- **Active Internships:** Shall continue until completion (typically 4–12 weeks from termination notice).
- **Skill Programmes:** Ongoing batches shall complete and receive certifications.
- **Projects & Consultancy:** May continue if parties mutually agree or be concluded within 60 days.

Head of the Department
Computer Science & Engineering
Madanapalle Institute of Technology & Science
MADANAPALLE-517 325.

RAMPEX TECHNOLOGIES PVT. LTD. ERODE

10. SIGNATORIES

This MoU is executed by authorized representatives of both parties:

For Madanapalle Institute of Technology & Science MITS *(deemed-to-be)* University:

Name	**Dr. M. Sreedevi**
Designation	**HOD**
Signature & Seal	Head of the Department Computer Science & Engineering Madanapalle Institute of Technology & Science MADANAPALLE-517 325.
Date	**21st January, 2026**

For RAMPEX TECHNOLOGIES:

Name	**Mr. K S Prasanth**
Designation	**Director** For RAMPEX TECHNOLOGIES LLP
Signature & Seal	Designated Partner
Date	**21st January, 2026**

11. WITNESS & ACKNOWLEDGMENT

MITS Representative:

Name	**Dr. R.Sudhakar**
Designation	**Single Point of Contact (SPOC)**
Signature	
Date	**21th January, 2026**

RAMPEX TECHNOLOGIES Representative:

Name	**Mr. Vikraam**
Designation	**Single Point of Contact (SPOC)**
Signature	
Date	**21st January, 2026**

EXECUTION CLAUSE

This Memorandum of Understanding is executed on the date of final signature below and shall be effective immediately. Both parties agree to be bound by the terms and conditions herein and to work collaboratively in good faith to achieve the objectives outlined.

Executed on: 21st January, 2026

Place: MITS (deemed to be) University, Madanapalle

Head of the Department
Computer Science & Engineering
Madanapalle Institute of Technology & Science
MADANAPALLE-517 325.

RAMPEX TECHNOLOGIES LLP ERODE

MITS University/ IRC/DL/MOU/26/02/019-CSE

MITS
MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(Deemed to be University under Section 3 of UGC Act, 1956)

ROOMAN
INNOVATE INTEGRATE EMPOWER

MEMORANDUM OF UNDERSTANDING (MoU)

Between
Madanapalle Institute of Technology & Science (*deemed-to-be*) University Madanapalle, Andhra Pradesh, India. *(A Deemed-to-be University offering Engineering, Management, MCA, and M. Tech* and Ph.D. programs*)*
and
ROOMAN TECHNOLOGIES #15B, Electronic city phase -1, Veer Sandra, (Near HCL Technologies), Bengaluru – 560100.

1. PREAMBLE

This Memorandum of Understanding (MoU) is entered into on the date of signature below between **Madanapalle Institute of Technology & Science (*deemed-to-be) University*, Madanapalle, Andhra Pradesh, India-517325,** a premier academic institution offering Engineering (B.Tech), Management (MBA), Master of Computer Applications (MCA), and Master of Technology (M.Tech) programs and Ph.D. programs with research and consultancy mandates, and **ROOMAN TECHNOLOGIES** #15B, Electronic city phase -1, Veer Sandra, (Near HCL Technologies), Bengaluru – 560100, an industrial training venture organization based in Location, State, Engaged in providing training services in the Engineering and Management sectors as a training partner.

2. PURPOSE OF THE MoU

The purpose of this MoU is to establish a formal framework between MITS University & ROOMAN TECHNOLOGIES to:

1. Facilitate structured internships, apprenticeships, and project-based learning for MITS students aligned to ROOMAN TECHNOLOGIES 's operational and strategic needs
2. Enable faculty engagement in consultancy, applied research, process improvement, and product innovation
3. Support ROOMAN TECHNOLOGIES 's digital transformation, quality improvement, sustainability, and export-readiness objectives
4. Facilitate regular knowledge-sharing, expert talks, workshops, and industry–academia networking
5. Establish a pathway for ongoing, mutually beneficial collaboration in emerging technologies (Development, Cloud, DevOps, Programming and Problem Solving using DSA)

3. SCOPE & KEY COLLABORATION AREAS

3.1 Student Internships & Project-Based Learning

- **Internships:** MITS shall nominate qualified students for semester-long or project-based internships at ROOMAN TECHNOLOGIES facilities.

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- Duration: Typically, 04 to 06 weeks for 3rd year Students and 04 to 06 months for Final year Students, aligned to MITS academic calendar.
- Student selection: Based on academic merit, skill fit, and ROOMAN TECHNOLOGIES specific requirements.
- Supervision: Dual mentorship by MITS faculty and ROOMAN TECHNOLOGIES technical lead.

- **Capstone & Industry-Guided Projects:** Student teams (UG, PG, MBA, M. Tech) undertake live projects addressing ROOMAN TECHNOLOGIES challenges in the areas of Process optimization, product design, digital systems, quality improvement, supply chain, sustainability
  - Deliverables: Feasibility reports, prototypes, software/simulation tools, recommendations.
  - Ownership: Academic work retained by students/MITS; ROOMAN TECHNOLOGIES gains first right to implement findings
- **Plant Visits & Exposure:** Organized facility tours and shopfloor orientation for MITS students to understand ROOMAN TECHNOLOGIES operations, technologies, and industry best practices.

3.2 Faculty Consultancy & Research Collaboration

- **Consultancy Projects:** MITS faculty engage with ROOMAN TECHNOLOGIES on:
  - Process optimization and efficiency improvement
  - Product innovation and design iteration
  - Quality systems, automation, Industry 4.0 adoption
  - Sustainability and circular economic initiatives
- **Research Partnerships:** Joint research proposals in emerging areas:
  - ROOMAN TECHNOLOGIES identifies R&D challenges; MITS proposes solutions.
  - Joint funding through government schemes (DST, DBT, IMPRINT, ICMR, etc.) or ROOMAN TECHNOLOGIES sponsorship.
  - Co-authored publications (subject to confidentiality agreements).
- **Faculty Sabbaticals:** MITS may nominate 1–2 faculty annually for 4 to 8 weeks industry immersions at ROOMAN TECHNOLOGIES for:
  - Technology capability assessment
  - Curriculum enhancement insights
  - Industry problem-solving participation

3.3 Skill Development Programme & Workforce Upskilling

- **Customized Training Programmes:** Co-design short-term, industry-specific courses (40–120 hours) if applicable for in the areas of EV battery systems, advanced manufacturing, quality management, digital systems, sustainability, supply chain:
  - ROOMAN TECHNOLOGIES employees (upskilling, cross-skilling)
  - MITS students (pre-placement / capability building)
  - Open-ended delivery as mutually agreed.
- **Certification & Credentials:** Programs shall carry ROOMAN TECHNOLOGIES 's endorsement and industry-recognized certifications (e.g., ISO, SIAM, FSAI, etc.) to enhance participant employability.

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- **Train-the-Trainer:** MITS develops trainer capacity within ROOMAN TECHNOLOGIES for sustainable, independent skill delivery post-MoU.

3.4 Placement & Recruitment Interface

- **Direct Placement Pathway:** ROOMAN TECHNOLOGIES considering evaluating MITS final-year students for direct placement/recruitment. MITS shall coordinate campus interviews, presentations, and candidate profiles.
- **Lateral Hiring & Internship-to-Job:** High-performing interns may be offered internship-extension or direct employment as per ROOMAN TECHNOLOGIES policy and MITS recommendations.

3.5 Innovation & Technology Partnership

- **Problem-Solving Collaboration:** MITS research centers, labs, and maker spaces available to ROOMAN TECHNOLOGIES for:
  - Prototype development and testing
  - CAD design and simulation
  - Material testing and quality analysis
  - Digital solution development (software, IoT, AI/ML)
- **Industry 4.0 & Digital Transformation:** MITS faculty assist ROOMAN TECHNOLOGIES in:
  - Automation readiness assessment
  - Digital process mapping
  - IoT / data analytics implementation consulting

3.6 Thought Leadership & Knowledge Dissemination

- **Expert Talks & Webinars:** ROOMAN TECHNOLOGIES executives deliver monthly/quarterly guest lectures at MITS on industry trends, case studies, and leadership insights.
- **Workshop Series:** Quarterly workshops (joint design) on sector-specific topics (e.g., "Advanced Manufacturing," "EV Systems," "Food Safety Compliance") open to MITS students and faculty.
- **Case Studies & Documentation:** Success stories from collaboration documented for:
  - MITS placement cell and student communication
  - ROOMAN TECHNOLOGIES marketing and CSR visibility
  - Academic publications (with confidentiality protections)

4. ROLES & RESPONSIBILITIES

4.1 MITS Shall

1. **Student Nomination & Coordination:** Identify, nominate, and monitor MITS students for internships, projects, and visits. Ensure academic standards, attendance, and learning outcome documentation.
2. **Faculty Engagement:** Nominate qualified faculty for consultancy, research, and teaching assignments. Coordinate their availability and ensure knowledge transfer back to curricula.

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3. **Infrastructure & Labs:** Provide access to laboratories, testing facilities, CAD suites, and workshops for:
    - Student project execution
    - Faculty consultancy and prototyping
4. **Quality Assurance:** Maintain academic rigor, ensure student conduct and safety compliance, and collect feedback from **ROOMAN TECHNOLOGIES** on student and faculty performance.
5. **Documentation & Reporting:** Maintain records of internships, projects, consultancy outcomes, and participant feedback. Provide **quarterly progress reports** to ROOMAN TECHNOLOGIES designated contact.

4.2 ROOMAN TECHNOLOGIES Shall

1. **Internship & Project Hosting:** Identify internship positions and live project opportunities for MITS students. assign mentors, provide facilities, and document learning outcomes.
2. **Student & Faculty Feedback:** Provide structured feedback on student/faculty performance, identifying strengths and areas for improvement. Share this with MITS for curriculum enhancement.
3. **Recruitment & Placement:** Participate in MITS campus recruitment drives (as per annual agreement). Interviewing final-year students and extend placement offers competitive talent.
4. **Guest Lectures & Workshops:** Nominate industry experts to deliver monthly guest talks and co-design quarterly workshops for MITS students and faculty.
5. **Knowledge Sharing:** Share (within confidentiality limits) process descriptions, technical specifications, industry challenges, and best practices to inform MITS curriculum and research focus.
6. **Coordination & Communication:** Designate a **Single Point of Contact (SPOC)** from ROOMAN TECHNOLOGIES 's HR/Operations/Innovation team to manage all coordination with MITS.
7. **Commitment Letters:** Provide annual **Letter of Intent (LOI)** specifying:
    - Number of internship positions and project opportunities
    - Preferred internship duration and timing (aligned to MITS semesters)
    - Skill/discipline focus (e.g., Mechanical, Electrical, Manufacturing, Software, etc.)
    - Placement targets and recruitment timeline
    - Training Programme participation (if any)

5. GOVERNANCE & REVIEW MECHANISM

5.1 Collaboration Oversight (Coordination, Not Hierarchical)

A **Collaboration Coordination Group (CCG)** shall be constituted to oversee implementation and ensure mutual satisfaction:

MITS Representatives:

- Vice Chancellor/ Assistant Director, Industry Relations / Dean/ HoD
- One Faculty Nominee (rotating annually by department)
- Dean, Academic Affairs (optional, for curriculum matters)

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Madanapalle Institute of Technology & Science
MADANAPALLE-517 325.

ROOMAN TECHNOLOGIES Representatives:

- Single Point of Contact (SPOC) – HR / Operations / Innovation Lead
- Department Head or Manager (where students are placed)
- (Optional) Sustainability or R&D Lead (if Innovation projects are active)

5.2 Joint Meetings & Functions

- **Frequency: Bi-annual** (June and December) + ad-hoc meetings as needed (virtual or in-person).
- **Venue:** Alternating between **MITS University** and **ROOMAN TECHNOLOGIES** facilities (or virtual).
- **Key Agenda Items:**
  - Track internship placements, student feedback, and **ROOMAN TECHNOLOGIES** satisfaction
  - Review project completions and deliverables
  - Monitor skill Programme participation and outcomes
  - Plan next-cycle placements, visits, and training initiatives
  - Address operational challenges, grievances, and bottlenecks
  - Update annual commitments and Letter of Intent for subsequent year

6. DURATION & RENEWAL

1. **Initial Term:** This MoU shall be valid for **two (2) years** from the date of execution.
2. **Automatic Renewal:** Upon expiry, the MoU shall automatically renew for successive **two-year periods**, unless either party provides written notice of non-renewal **at least 60 days** prior to expiry.
3. **Amendment:** Any modifications to the scope, terms, or obligations may be made by mutual written consent, documented via an amendment addendum signed by both parties.
4. **Continuation of Ongoing Activities:**
   - Any student internships in progress at the time of termination shall **continue to be completed.**
   - Ongoing projects shall be concluded or handed over as mutually agreed.
   - Final documentation and settlement shall be completed within 60 days of termination notice.

7. FINANCIAL ARRANGEMENTS

1. **No Monetary Exchange Between Institution & ROOMAN TECHNOLOGIES:** This MoU does not involve financial payments between MITS and ROOMAN TECHNOLOGIES. Each party bears its own operational costs.
2. **Internship & Project Engagement:**
   - **ROOMAN TECHNOLOGIES may provide:** Stipend to interns (optional, as per ROOMAN TECHNOLOGIES policy), facility access, materials for projects.
   - **MITS may provide:** Student working, faculty expertise, laboratory facilities (at concessional/no cost as a partnership contribution).
3. **Consultancy Projects:**
   - Consulting rates (if applicable to faculty R&D) shall be agreed in a **separate Consulting Agreement** for each project, typically at 20–40% below market rate as a partnership incentive.

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4. **Jointly Funded R&D:**
    - Both parties may jointly apply for government grants (DST, DBT, IMPRINT, ICMR, etc.).
    - Cost-sharing and funding allocation to be documented in project-specific MOUs.

8. CONFIDENTIALITY & INTELLECTUAL PROPERTY

8.1 Confidential Information (Mutual Protection)

Both parties may share sensitive business, technical, or process information during collaboration. Such information shall:

- **Be used solely** for purposes outlined in this MoU
- **Be protected from unauthorized disclosure** by both parties and their representatives
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- ROOMAN TECHNOLOGIES shall not disclose MITS student information without explicit student consent.
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9. TERMINATION & DISPUTE RESOLUTION

9.1 Termination Provisions

1. **Mutual Termination:** Both parties may agree to terminate by mutual written consent, with clear documentation of reasons and transition plan.
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- **Active Internships:** Shall continue until completion (typically 4–12 weeks from termination notice).
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Computer Science & Engineering
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MADANAPALLE-517 325.

10. SIGNATORIES

This MoU is executed by authorized representatives of both parties:

For Madanapalle Institute of Technology & Science MITS *(deemed-to-be)* University:

Name	**Dr. M. Sreedevi**
Designation	**HOD**
Signature & Seal	Head of the Department Computer Science & Engineering Madanapalle Institute of Technology & Science MADANAPALLE-517 325.
Date	**20th February, 2026**

For ROOMAN TECHNOLOGIES:

Name	**Mr. Rakshit Shetty**
Designation	**Director**
Signature & Seal	
Date	**20th February, 2026**

11. WITNESS & ACKNOWLEDGMENT

MITS Representative:

Name	**Dr. R. Nidhya**
Designation	**Single Point of Contact (SPOC)**
Signature	R-Nidhya
Date	**20th February, 2026**

ROOMAN TECHNOLOGIES Representative:

Name	**Mr. Venkat B Reddy**
Designation	**Single Point of Contact (SPOC)**
Signature	Vready
Date	**20th February, 2026**

EXECUTION CLAUSE

This Memorandum of Understanding is executed on the date of final signature below and shall be effective immediately. Both parties agree to be bound by the terms and conditions herein and to work collaboratively in good faith to achieve the objectives outlined.

Executed on: 20th February, 2026
Place: MITS (deemed to be) University, Madanapalle

Department of Computer Science & Engineering

List of Hands-on Training and Skill Development Programmes for the Academic Year 2025–2026

S.No.	Date	Event Details	No. of. Participants
1	19.09.2025 to 20.09.2025	Two-days Hands on Workshop on "The DevOps Vanguard: Tools, Tactics & Transformation"	59
2	29.12.2025 to 03.01.2026	One Week Domain-Specific Hands-On Workshop on "Full Stack Web Development using Modern Frameworks"	72
3	05.01.2026 to 10.01.2026	One Week Domain-Specific Hands-On Workshop on "Building Intelligent Systems Using Machine Learning Techniques"	71
4	28.1.2026 to 29.1.2026	Two Days hands-on "IoT and AI Training on AI & IoT Powered Intelligent System for Real-Time Robot Movement Detection"	70
5	30.01.2026	One day Workshop on "CSI AI 100K- AI Readiness Session"	768
6	03.02.2026 to 07.02.2026	Five-Day Hands-On Skill Development Program on "MERN Stack from Fundamentals to Industry-Ready Full-Stack Application Development"	71
7	05.02.2026 to 06.02.2026	Two Days Professional Development Programme Hands on Training titled "Series: -I Advanced Java for Industry-Ready Application Development"	680
8	04.03.2026 to 18.03.2026	AWS – Amazon Web Services – Global Certificate Training Programme	32

HoD/CSE

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MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(Deemed to be University)
Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi
NAAC Accredited with A+ Grade, NIRF India Rankings 2024 - Band: 201-300 (Engg.)
NBA Accredited - B.Tech. (CIVIL, CSE, ECE, EEE, MECH, CST), MBA & MCA

A Report on Two-days Hands on Workshop on "The DevOps Vanguard: Tools, Tactics & Transformation" Organized by Department of Computer Science & Engineering in association with ISTE Student Chapter on 19.09.2025 & 20.09.2025





Report Submitted by: Mr. K. Sathish, Assistant Professor, Department of Computer Science & Engineering.

Organized by: Mr. K. Sathish, Assistant Professor, Dept. of CSE. Dr. K. Sree Divya, Assistant Professor

Resource Person Details: Mr. B. Surendra Reddy, Software Developer, Capgemini Technology Chennai.
Attended: Third year ISTE Chapter Students from MITS (Total Count: 59)
Venue: Scale Up Class Room, MITS
Mode of Conduct: Offline

Event Overview:

The Department of Computer Science & Engineering, in association with the ISTE Student Chapter, organized a Two-days hands-on workshop titled **"The DevOps Vanguard: Tools, Tactics & Transformation"** on **19th and 20th September 2025**. This workshop was aimed at equipping participants with in-depth knowledge and practical experience in modern DevOps methodologies, tools, and best practices.

Designed to bridge the gap between theoretical understanding and real-world implementation, the event provided students with a comprehensive learning platform focusing on the continuous integration and delivery (CI/CD) pipeline, infrastructure automation, containerization, monitoring, and collaboration tools that are central to DevOps.

Over the Two days of the workshop featured:

1. **Foundational DevOps Concepts**: Introduction to DevOps culture, principles, and lifecycle, emphasizing the collaboration between development and operations teams to enhance software delivery and reliability.
2. **Toolchain Exploration**: Hands-on exposure to widely-used DevOps tools including Git, Jenkins, Docker, Kubernetes, Ansible, and monitoring tools like Prometheus and Grafana.
3. **Real-Time Implementation**: Practical sessions on setting up CI/CD pipelines, container orchestration using Docker and Kubernetes, and infrastructure automation, allowing participants to apply concepts in real-world scenarios.
4. **Expert-Led Discussions**: Interactive sessions led by industry professionals and experienced developers, offering insights into DevOps trends, real-world challenges, enterprise implementations, and emerging career opportunities.

This immersive workshop enabled students to develop a strong foundation in DevOps practices and gain the technical proficiency required to excel in a fast-evolving software engineering landscape. It also fostered a collaborative learning environment, encouraging innovation, critical thinking, and a deeper understanding of the DevOps mindset.

Inauguration

The Two-Days Hands on workshop, **"The DevOps Vanguard: Tools, Tactics & Transformation"** was officially inaugurated on 19th January 2025 with an engaging welcome address delivered by Poojitha, a Third-year CSE student. In her speech, Poojitha expressed gratitude to the Management, Honourable Vice Chancellor, Respected Principal, Dean, Head of the Department, and the coordinators for their unwavering support, which made the event possible. She also warmly welcomed the resource person for the workshop, Mr. B. Surendra Reddy, Software Developer, Capgemini Technology Chennai, who brought with him six years of expertise in DevOps Technology. Mr. Surendra experience in working on various DevOps projects made him the ideal person to lead the sessions throughout the workshop.

Dr. M. Sreedevi, Head of the Department, also welcomed the participants, highlighting the importance of such initiatives in shaping their skills. The inauguration was further enriched by an insightful address from Dr. Chandra Prakash Gupta, Professor & Dean who emphasized the significance of mastering DevOps for success in the software industry.





Resource Person Profile:

Mr. B. Surendra Reddy is a skilled DevOps Engineer with over 6.6 years of experience in CI/CD pipelines, automation, containerization, and cloud infrastructure. He currently works at Capgemini Technologies as a Software Developer (DevOps) and serves as the Single Point of Contact (SPOC) for the Ericsson MTAS project, focusing on test integration and environment management.

He is proficient in a wide range of DevOps tools and technologies including Jenkins, Docker, Kubernetes, Ansible, and AWS, and is known for collaborating effectively across teams to streamline workflows and resolve production issues.

Mr. Surendra Reddy holds both Bachelor's (B. Tech) and Master's (M. Tech) degrees from JNTU Anantapur. He is passionate about sharing knowledge and regularly conducts hands-on training sessions and workshops, helping professionals adopt best practices in automation and deployment. He has been recognized with the prestigious WOW Award at Capgemini for his outstanding performance and commitment to CI support and issue resolution. Additionally, he is a WIPRO Certified Faculty and a Mentor for Project-Based Learning in Java J2EE.

Day 1 Session:

The First day of the Two-Days Hands-on workshop "**The DevOps Vanguard: Tools, Tactics & Transformation**" laid a strong foundation for participants by introducing the core principles and practices of DevOps, along with essential tools and scripting skills.

1. Introduction to DevOps

The session began with an insightful overview of DevOps, highlighting its significance in modern software development. Participants were introduced to the core objectives of DevOps—enhancing collaboration between development and operations, automating workflows, and accelerating software delivery with high reliability. The facilitator discussed the DevOps lifecycle, emphasizing key phases such as planning, development, testing, deployment, and monitoring.

2. Agile Process

The second session focused on the Agile methodology, a foundational component of the DevOps culture. Students learned how Agile practices promote iterative development, continuous feedback, and customer-centric delivery. The discussion also covered Scrum frameworks, sprint planning, daily stand-ups, and retrospectives, illustrating how Agile and DevOps complement each other in achieving faster and more efficient product development.

3. Linux Scripting Commands

A practical hands-on session followed, where students were introduced to essential Linux commands and shell scripting. This session aimed at building proficiency in navigating Linux environments—a critical skill for DevOps professionals. Topics covered:

- Basic command-line operations
- File permissions and directory structures
- Process management
- Writing and executing simple shell scripts

4. DevOps Tools Overview

The final session of Day 1 provided a broad overview of popular **DevOps tools** used across the CI/CD pipeline. Students were introduced to:

- **Version control systems** (Git)
- **Build automation tools** (Maven, Jenkins)
- **Containerization platforms** (Docker)
- **Configuration management tools** (Ansible)
- **Monitoring solutions** (Prometheus, Grafana)



Day 2 Session:

Building on the foundational knowledge from Day 1, The Second day of the **"The DevOps Vanguard: Tools, Tactics & Transformation"** workshop focused entirely on hands-on implementation of industry-standard DevOps tools and real-time project deployment in a cloud environment. The sessions were designed to give participants practical experience in setting up CI/CD pipelines, containerization, and deploying applications on cloud infrastructure.

1. DevOps Tools – Hands-on Practice

The day began with guided, interactive sessions where participants worked directly with various DevOps tools introduced during Day 1. The goal was to reinforce understanding through real-world application.

2. Jenkins – Continuous Integration

A detailed hands-on session on Jenkins introduced students to the concept of Continuous Integration (CI). Participants learned how to:

- Set up Jenkins jobs
- Automate build and test cycles
- Trigger builds from code changes in GitHub
- Integrate build pipelines with other DevOps tools

3. GitHub – Version Control & Collaboration

Students explored GitHub as a powerful platform for version control, team collaboration, and source code management. Key activities included:

- Creating repositories
- Pushing/pulling code
- Branching and merging workflows
- Linking GitHub with Jenkins for automated CI

4. Docker – Containerization Essentials

In this session, participants gained hands-on experience with Docker, learning how to:

- Create and manage Docker containers
- Build custom Docker images
- Write Dockerfiles
- Understand Docker architecture and use cases

5. Containerization Concepts

Beyond basic Docker commands, this segment covered the broader concept of containerization, its advantages over traditional virtual machines, and how it supports scalable and consistent deployment across environments.

6. AWS Instance – Cloud Deployment

A practical session was conducted on launching and configuring AWS EC2 instances. Participants learned how to:

- Set up a cloud server
- Install necessary software and dependencies
- Connect securely using SSH
- Prepare cloud infrastructure for deployment

7. Real-Time Project Deployment

The workshop concluded with a live deployment of a real-time application using the DevOps pipeline established throughout the day. Participants:

- Built the application
- Containerized it using Docker
- Deployed it to AWS using Jenkins and GitHub integration
- Monitored the running services and validated the deployment process

Vote of Thanks

The workshop concluded with a vote of thanks delivered by Dr. K. Sree Divya and

Mr. K. Sathish, who expressed their heartfelt appreciation to the resource person, management, and all the participants for their active involvement and contribution to the event's success.



Event Outcome:

1. Practical Understanding of Core DevOps Tools

- Gain hands-on experience with key DevOps tools such as Git, Jenkins, Docker, Kubernetes, Ansible, and Terraform.
- Learn how to integrate and automate using CI/CD pipelines.
- Understand real-world use cases and configurations.

2. Building a CI/CD Pipeline from Scratch

- Design and implement a complete CI/CD pipeline.
- Automate code testing, building, deployment, and monitoring.
- Understand version control workflows (e.g., GitOps, trunk-based development).

3. Cloud-Native DevOps Strategies

- Deploy and manage applications on cloud platforms (AWS, Azure, or GCP).
- Learn container orchestration with Kubernetes in a cloud environment.
- Practice infrastructure as code (IaC) using tools like Terraform.

4. DevOps Culture & Agile Alignment

- Explore the cultural shift required for DevOps adoption.
- Learn how DevOps complements Agile and Lean methodologies.
- Understand collaboration models between development, QA, and operations.

5. Monitoring, Logging, and Incident Response

- Implement observability tools (e.g., Prometheus, Grafana, ELK stack).
- Set up basic alerting and dashboards.
- Learn best practices for incident handling and SRE principles.

6. Security & Compliance in DevOps (DevSecOps)

- Integrate security checks into the CI/CD process.
- Learn secure coding, vulnerability scanning, and policy as code.
- Practice compliance automation using tools like SonarQube or Snyk.

7. Real-World Case Studies & Use Cases

- Analyze successful DevOps transformations.
- Understand challenges, trade-offs, and best practices.
- Learn how to tailor DevOps practices to their organization.

8. Personal and Team-Level Growth

- Enhance cross-functional collaboration skills.
- Leave with project-ready templates, code, and workflows.
- Be equipped to initiate or improve DevOps practices in their workplace.

Newspaper Clips:



ఈనాడు
epaper.eenadu.net

సాఫ్ట్‌వేర్ అభివృద్ధిపై దృష్టి సారించండి

విద్యార్థులకు అవగాహన కల్పిస్తున్న సాఫ్ట్‌వేర్ నిపుణుడు సురేంద్రరెడ్డి

మదనపల్లె విద్య, న్యూస్‌టుడే : ఇంజినీరింగ్ విద్యార్థులు సాఫ్ట్‌వేర్ అభివృద్ధిపై దృష్టి సారించాలని చెన్నైకు చెందిన ఓ కంపెనీ సాఫ్ట్‌వేర్ నిపుణుడు బి. సురేంద్రరెడ్డి అన్నారు. శుక్రవారం మిట్స్ డీమ్డ్ టుబీ విశ్వవిద్యాలయంలో కంప్యూటర్ సైన్స్ అండ్ ఇంజినీరింగ్ విద్యార్థులకు శిక్షణ కార్యక్రమం నిర్వహించారు. కార్యక్రమంలో డీన్ డాక్టర్ చంద్ర ప్రకాష్ గుప్త, విభాగాధిపతి డాక్టర్ శ్రీదేవి, సమన్వయకర్త కె.సతీష్, విద్యార్థులు, అధ్యాపకులు పాల్గొన్నారు.

Date : 20/09/2025 EditionName : ANDHRA PRADESH(ANNAMAYYA) PageNo :

MITS
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A Report on
One Week Domain-Specific Hands-On Workshop on Full Stack Web Development using Modern Frameworks
Organized by
Department of Computer Science & Engineering
In Association with ISTE Student Chapter



Dates: 29.12.2025 – 03.01.2026
Time: 09:30 AM – 4:30 PM
Venue: SRB – 308
Organized by: Department of Computer Science & Engineering
In Association with: MITS ISTE Student Chapter
Resource Person: Mr. Prashanth K S, Full Stack Developer, Rampex Technologies, Coimbatore

Report Submitted by: Mrs. G.Vasundhara Devi Assistant Professor/CSE

Event Overview

Title: One Week Domain-Specific Hands-On Workshop on Full Stack Web Development using Modern Frameworks

The program commenced at 9:30 AM with a warm welcome address delivered by Mrs. G.Vasundhara Devi, Assistant Professor, Department of CSE. She cordially welcomed the dignitaries, resource person, faculty members, and students. She briefly outlined the purpose of organizing the One Week Hands-on Workshop on "Full Stack Web Development using Modern Frameworks" and emphasized its relevance in today's fast-evolving IT industry.

Following the welcome address, Dr. M. Sreedevi, Professor and Head, Department of CSE, highlighted the growing demand for skilled Full Stack Developers in the software industry. She stressed the importance of mastering both frontend and backend technologies to build scalable, secure, and user-friendly web applications. She encouraged students to actively participate in the sessions, engage in hands-on practice, clarify their doubts, and utilize the workshop as an opportunity to strengthen their practical development skills.

The session was then handed over to the resource person, Mr. Prashanth K S, Full Stack Developer, Rampex Technologies, Coimbatore. He began by expressing his sincere gratitude to the organizing committee, the Head of the Department, the Principal, and the Management of MITS Madanapalle for providing him with the opportunity to interact with the students. He appreciated the enthusiasm and interest shown by the participants.

The resource person introduced the fundamentals of Full Stack Web Development, explaining the integration of frontend technologies such as HTML, CSS, JavaScript, and modern frameworks with backend technologies including Node.js, Express.js, and database systems. He shared insights on real-time application development, industry practices, project deployment strategies, and current
trends in modern web technologies.

Program Objectives

The primary objectives of the program were:

• To introduce students to the fundamentals and advanced concepts of Full Stack Web Development.
• To provide hands-on experience in building responsive and dynamic web applications.
• To bridge the gap between theoretical knowledge and real-time industry implementation.
• To enable students to design, develop, test, and deploy complete web applications.
• To enhance coding proficiency in modern frontend and backend frameworks.
• To improve students' problem-solving abilities through mini project development.
• To strengthen placement readiness in web development and software engineering domains.

Day 1: Fundamentals of Web Development

Session Topics:

Introduction to HTML5, CSS3
Responsive Web Design concepts
Basic JavaScript
Creating static web pages

Day 2: Advanced Frontend Development

Session Topics:

Introduction to Modern JavaScript (ES6+)
Frontend Framework Overview (React/Angular concepts)
Component-based architecture
Building interactive UI components





Day 3: Backend Development

Session Topics:

Introduction to Node.js
Express.js framework
RESTful APIs
Database connectivity (MongoDB/MySQL basics)

Day 4: Frontend–Backend Integration

Connecting APIs with Frontend
Authentication & Authorization Basics
CRUD Operations
Error Handling & Debugging
Hands-on: Full Stack Application Integration

Day 5: Mini Project Development

Project Team Formation
Requirement Analysis
UI Design & Database Schema
Coding & Implementation

Resource Person Interaction & Guidance





Day 6: Project Presentation & Valedictory

Student Project PPT Presentation
Live Demo of Web Applications
Feedback & Technical Suggestions by Resource Person
Interaction Session
Certificate Distribution

Outcomes of the Program

The Six-Day Hands-on Workshop on "Full Stack Web Development using Modern Frameworks" yielded significant academic and professional benefits for the participants. The major outcomes of the program are as follows:

1. **Enhanced Technical Competency:**
   Students acquired comprehensive knowledge of both frontend and backend technologies, including modern web frameworks, server-side development, and database integration.

2. **Practical Implementation Skills:**
   Participants gained hands-on experience in designing, developing, testing, and integrating full stack web applications, thereby strengthening their practical coding abilities.



3. **Project Development Experience:**
   Students successfully developed and demonstrated mini projects, enabling them to understand real-time application architecture and workflow.

4. **Improved Problem-Solving and Debugging Skills:**
   Through continuous practice and mentor guidance, students enhanced their ability to identify errors, debug code, and optimize performance.

5. **Teamwork and Communication Skills:**
   The project-based learning approach encouraged collaboration, teamwork, and effective technical presentation skills.

6. **Industry Exposure and Career Awareness:**
   The resource person shared insights into current industry trends, deployment strategies, and employer expectations, thereby improving students' industry readiness.

7. **Portfolio and Placement Readiness:**
   The mini projects developed during the workshop can be included in students' professional portfolios, strengthening their placement and internship prospects.

8. **Increased Confidence:**
   Students gained confidence in developing and presenting complete full stack applications independently.

Overall, the workshop successfully achieved its objectives by bridging the gap between theoretical knowledge and practical implementation, thereby enhancing students' technical proficiency and employability in the domain of full stack web development.

Mr. B. Galeebathullah, Assistant Professor, Department of CSE, delivered the Vote of Thanks. He expressed sincere gratitude to the Management, Principal, Head of the Department, and the resource person for their support in successfully conducting the Six-Day Workshop on "Full Stack Web Development using Modern Frameworks." He also appreciated the active participation of the students. The program concluded with the distribution of certificates and a positive note to organize more such skill-oriented workshops in the future.

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A Report on
One Week Domain-Specific Hands-On Workshop on "Building Intelligent Systems Using Machine Learning Techniques"

Organized by

Department of Computer Science & Engineering
In Association with IEEE



Date: 05.01.2026 to 10.01.2026
Mode of Conduct: Offline
Venue: SRB308
Total Participants: 71 (Internal)
Coordinators: Mrs. M. Bommy and Mr. E. Rajesh
Resource Person: Mr. Premmaran Ganesh, Senior Engineer, Tata Consultancy Services, Chennai.

Report Submitted by:
Mrs. Bommy M, Assistant Professor/CSE

Event Overview

Title: One Week Domain-Specific Hands-On Workshop on "Building Intelligent Systems Using Machine Learning Techniques"

The program commenced at 9:30 AM with a warm welcome address delivered by Mrs. M. Bommy, Assistant Professor, Department of CSE. She greeted the dignitaries, resource person, faculty members, and students, and briefly outlined the purpose of organizing the workshop.
Following the welcome address, **Dr. M. Sreedevi**, Professor and Head, Department of CSE, emphasized the growing significance of Machine Learning in today's technology-driven world. She highlighted the importance of conducting the workshop on "Building Intelligent Systems Using Machine Learning Techniques" to equip students with industry-relevant skills. She encouraged the participants to actively engage in the sessions, clarify their doubts, and make the best use of the hands-on learning opportunities provided during the workshop.
The session was then handed over to the resource person, Mr. Premmaran Ganesh. He began by expressing his sincere gratitude to the organizing committee, the Head of the Department, the Principal, and the Management of MITS Madanapalle for providing him with the opportunity to share his knowledge and expertise. He also appreciated the enthusiastic participation of the students. The resource person then introduced the core concepts of building intelligent systems using Machine Learning techniques and shared his insights on practical implementation, real-world applications, and emerging trends in the field.

Program Objectives:

The primary objectives of the program were:

- To introduce students to the fundamentals and advanced concepts of Machine Learning.
- To provide hands-on experience in building intelligent systems using real-world datasets.
- To bridge the gap between theoretical knowledge and practical implementation.
- To enable students to design, train, evaluate, and optimize ML models.
- To develop problem-solving skills through mini project development.
- To enhance placement readiness and research orientation in AI/ML domains.

Day 1: Foundations of Intelligent Systems & Python for ML

Session Topics:

- Introduction to Artificial Intelligence and Machine Learning
- Understanding Intelligent Systems
- Applications of ML in Real-World Scenarios
- Python Programming for Machine Learning
- Introduction to NumPy and Pandas
- Data Types and Data Structures
- Data Loading and Basic Preprocessing

Hands-On Activities:

- Installing required libraries
- Working with datasets using Pandas
- Handling missing values
- Data cleaning techniques
- Basic data visualization using Matplotlib

Day 2: Supervised Learning – Regression Techniques

Session Topics:

- Introduction to Supervised Learning
- Understanding Regression Problems
- Linear Regression
- Multiple Linear Regression
- Model Training and Testing
- Cost Function and Gradient Descent (Conceptual)
- Model Evaluation Metrics (MSE, RMSE, R^2 Score)

Hands-On Activities:

- Implementing Linear Regression using Scikit-learn
- Splitting dataset into training and testing sets
- Visualizing regression line
- Evaluating regression model performance

Day 3: Supervised Learning – Classification Techniques

Session Topics:

- Introduction to Classification Problems
- Logistic Regression
- K-Nearest Neighbors (KNN)
- Decision Trees
- Naïve Bayes Classifier
- Confusion Matrix
- Precision, Recall, F1-Score

Hands-On Activities:

- Building classification models
- Evaluating model performance
- Comparing multiple algorithms
- Implementing a Spam Detection / Student Performance Prediction model

Day 4: Advanced Machine Learning & Feature Engineering

Session Topics:

- Feature Selection Techniques
- Feature Scaling (Normalization & Standardization)
- Support Vector Machines (SVM)
- Random Forest Algorithm
- Hyperparameter Tuning
- Cross-Validation
- Overfitting and Underfitting

Hands-On Activities:

- Applying feature scaling
- Training Random Forest model
- Performing cross-validation
- Model comparison and optimization

Day 5: Unsupervised Learning & Dimensionality Reduction

Session Topics:

- Introduction to Unsupervised Learning
- Clustering Concepts
- K-Means Clustering
- Hierarchical Clustering (Overview)
- Principal Component Analysis (PCA)
- Anomaly Detection Basics

Hands-On Activities:

- Implementing K-Means Clustering
- Visualizing clusters
- Applying PCA for dimensionality reduction
- Building a simple anomaly detection system

Day 6: End-to-End Intelligent System Development & Deployment

Session Topics:

- Machine Learning Pipeline
- Model Evaluation and Interpretation
- Introduction to Model Explainability
- Deployment Basics (Flask / Streamlit Overview)
- Real-World Case Studies
- Resume Building & Interview Preparation in ML Domain

Hands-On Activities:

- Developing an end-to-end ML project
- Integrating preprocessing with model and evaluation
- Deploying a simple ML application locally

Mini Project

Students developed an Intelligent System such as:

- Spam Detection System
- Intrusion Detection System
- Student Performance Prediction System
- House Price Prediction Model
- Customer Segmentation System
- Sentiment Analysis Application

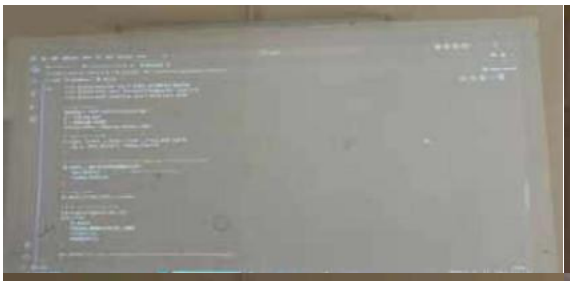
Outcome of the Workshop:

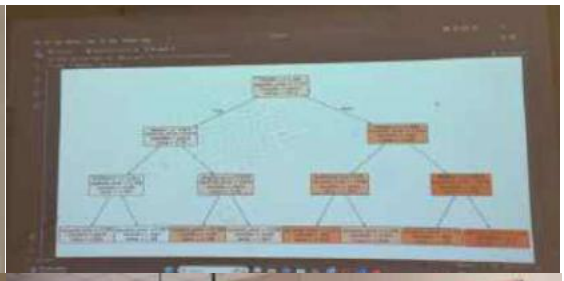
The One Week Domain-Specific Hands-On Workshop on "Building Intelligent Systems Using Machine Learning Techniques" effectively fulfilled its planned objectives, resulting in the following key outcomes:

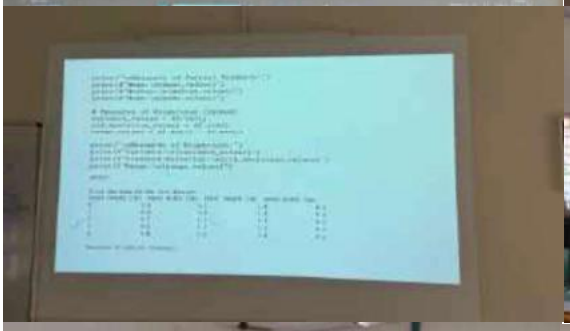
- Students gained a strong understanding of Machine Learning concepts and intelligent system design.
- Participants developed hands-on skills in data preprocessing, model building, and performance evaluation.
- Students successfully implemented regression, classification, and clustering algorithms on real-world datasets.
- The workshop enhanced problem-solving abilities and practical application skills in AI/ML domains.
- Participants improved their project development capability and placement readiness through mini project implementation.

The final day of the One Week Domain-Specific Hands-On Workshop on "Building Intelligent Systems Using Machine Learning Techniques" concluded with a formal valedictory session, marking the successful completion of the program. Feedback was collected through a structured questionnaire. Students felt that the workshop was highly informative, practical, and industry-relevant.

Later, Mr. E. Rajesh, Assistant Professor, Department of CSE delivered Vote of Thanks. He expressed his sincere gratitude to the Management, Principal, Head of the Department and the resource person for their support in making the workshop a grand success. He also appreciated the enthusiastic participation of the students throughout the six days. The program ended with the distribution of participation certificates and a positive note to conduct more such skill-oriented workshops in the future.





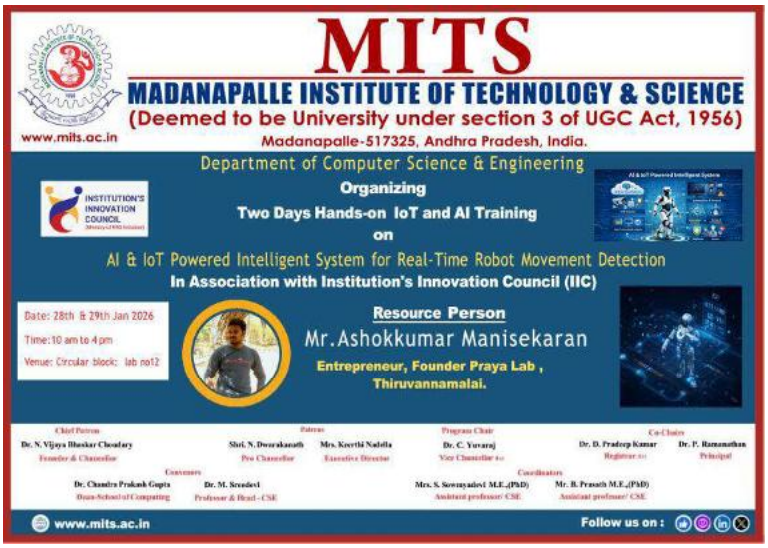








A Report on
"Two Days hands-on IoT and AI Training on AI & IoT Powered Intelligent System for Real-Time Robot Movement Detection"
Organized by **Department of Computer Science & Engineering**
In Association with IIC



Date: 28.1.2026 & 29.1.2026
Mode of Conduct: Offline
Venue: Lab 12 – Circular Block
Total Participants: 70

Report Submitted by:
Mrs. Sowmyadevi S & Mr.B. Prasath
Assistant Professor,
Department of Computer Science & Engineering

Event Overview

Title: Two Days Hands-on IoT and AI Training on *AI & IoT Powered Intelligent System for Real-Time Robot Movement Detection*

The **Two Days hands-on IoT and AI Training on "AI & IoT Powered Intelligent System for Real-Time Robot Movement Detection"** was successfully conducted. The program commenced at **10:00 AM** with a welcome address delivered by **Mrs. S. Sowmyadevi, M.E., (PhD), Assistant Professor, Department of CSE, MITS**.**Dr. M. Sreedevi, Head of the Department**, extended a warm welcome to the resource person and highlighted the importance of such workshops in building strong practical foundations in emerging technologies. She encouraged students to actively participate, learn new concepts, and explore innovation through hands-on experience. Following this, the session was handed over to **Mr. Ashok Kumar M.E.,** who guided the participants through an engaging, interactive, and practical training program.

Program Objective

The objective of the training was to help participants **design, build, and demonstrate an AI & IoT–enabled intelligent robotic system** using a **Funobotz paper-based Dog Bot**, where **real-time movement detection** triggers alerts and enables an automated robot response.

Training Use Case

A **mobile paper robot** detects movement using a **camera-based detection setup**. When movement is detected:

- A **buzzer sounds** (audio alert)
- An **LED flashes** (visual alert)
- The flashing light is sensed by an **LDR**
- The robot **automatically moves backward**

This use case demonstrates the complete pipeline of **real-time detection → alerting → intelligent response**.

Day 1 – Build and Understand the System

Focus: Understanding system flow and building a working robot with alert mechanisms.

Session 1: Introduction & System Thinking

Participants were introduced to the concept of an intelligent detection system and the real-time workflow of detection, alert, and automated response. The use case was discussed in detail, emphasizing how inputs, outputs, and decision flow interact in an intelligent IoT-enabled robot.

Session 2: Hardware Setup with Funobotz Paper Bot
Participants assembled the **Funobotz paper-based Dog Bot**, configured the motor setup for forward and backward movement, and learned how to mount the required sensors and indicators for an integrated robotic workflow.
Session 3: Sensor and Alert Integration
This session focused on integrating alert mechanisms such as the **buzzer** and **LED**, and understanding the **LDR** as a light-detection sensor. Participants learned basic threshold logic and how sensing and alerting are connected in an IoT response chain.
Session 4: Basic Control Logic
Participants implemented and tested simple control logic for LED ON/OFF, buzzer triggering, and motor movement. Each component was tested individually to ensure stable performance before integrating into a complete working system.
Day 2 – Intelligence, Automation and Demo
Focus: Adding detection logic and enabling the robot to respond automatically.
Session 5: Movement Detection Logic
Participants were introduced to camera-based movement detection, understanding how motion triggers events and how such events can be connected to alert actions.
Session 6: Automated Response Design
Participants implemented the automated response mechanism where LED flashing acts as a signal, the LDR detects the flash, and control logic drives the robot backward automatically. Delay handling and safety timing concepts were also discussed to ensure reliable execution.
Session 7: Real-Time Testing and Optimization
The complete system was tested live. Participants calibrated sensor thresholds, fine-tuned logic, and improved response accuracy through repeated trials and adjustments.
Session 8: Demonstration and Discussion
Teams demonstrated their working prototypes, explained the system workflow, and participated in discussions on real-world applications of intelligent detection systems. Suggestions for improvements and possible extensions were also shared.
Final Program Outcome
By the end of the training, participants were able to:

- Build a simple **AI & IoT powered intelligent system**
- Understand the logic of **real-time detection and automated response**
- Integrate sensors, alerts, and motors effectively
- Demonstrate a functional robot prototype
- Connect classroom learning to real-world intelligent systems and applications

The event concluded at **5:00 PM**, with participants sharing valuable feedback. Many participants expressed enthusiasm for the practical sessions and appreciated the resource person's engaging delivery style.

Event Highlights

- Resource person delivering the session



- Participants actively building an AI & IoT–enabled intelligent robotic system using the Funobotz paper-based Dog Bot

Vote of Thanks
The program ended with a Vote of Thanks delivered by **Mr. B. Prasath M.E.,(PhD).**, expressing gratitude to the resource person, participants, Head of the Department, Principal, and Management for their support and for making the event a grand success.
Key Advantage of the Program
A major advantage of this program is to enabling the participants to:

- Rebuild and modify the robot after training
- Practice independently
- Use the kit for projects, demonstrations, and competitions

Participants also received full access to the training content and gained meaningful hands-on experience throughout the sessions.
Key Outcomes Achieved
By the end of the training, participants achieved:

- A comprehensive understanding of AI & IoT–enabled intelligent robotic system concepts
- The ability to rebuild and modify the robot after training
- Practical skills best suited for skill development initiatives and innovation labs

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A Report on

One day Workshop on

"CSI AI 100K- AI Readiness Session"

Organized by

Department of Computer Science & Engineering

In Association with

Computer Society of India (CSI) Hyderabad Chapter

30.01.2026



Report Submitted by: Dr R Nidhya, Professor, Department of Computer Science & Engineering.

Event Co-Coordinator: Mr. G. Muthugurunathan, Assistant Professor, Department of Computer Science & Engineering; Mr. P. Kaliya Moorthi, Assistant Professor, Department of Computer Science & Engineering

Total no. of Participants: 768

Location & Mode of Event: Auditorium & Offline mode

Timings: 10 AM to 5 PM Report Received on 06.02.2026.

The **Department of Computer Science & Engineering**, Madanapalle Institute of Technology & Science (Deemed to be University), successfully organized a **One-Day Workshop on "CSI AI 100K- AI Readiness Session"** on **30 January 2026**, In association with CSI Hyderabad chapter. The workshop aimed to expose students and faculty to advanced concepts and real-time applications related to **AI**. The program commenced at 10 AM with a warm welcome address by Dr. R. Nidhya, the Event Coordinator, who highlighted the significance of the event. This was followed by an inspiring inaugural address by Dr. M Sreedevi, HoD/CSE MITS who emphasized the importance of the workshop.

M Chandra Sekhar, a Full Stack Java Trainer from TEKS Academy, Bengaluru, participated as the chief guest for the workshop. Speaking at the event, he stated that this program is part of CSI's goal to train one lakh young people in the field of artificial intelligence and aims to create awareness among students. He said that by dividing students into small groups, focusing on learning useful concepts in a short time, and working on real-life projects, they can gain proficiency in artificial intelligence. This, he added, would enhance the students' thinking abilities tenfold.

He mentioned program is designed in line with the state and central government's National Al Mission. Students and faculty participating in this program will receive training in new technologies such as Machine Learning, Generative Al, and Quantum Computing, and will be awarded a nationally recognized CSI digital certificate. He emphasized that students have ample job opportunities in IT companies as well as in data analytics, finance, and healthcare sectors, and urged them to take advantage of these opportunities.

The program concluded with a vote of thanks by Mr G Muthugurunathan, acknowledging CSI Hyderabad Chapter, institutional leadership, the resource person, coordinators, faculty members, and student volunteers for their support and cooperation in making the event a grand success.









Outcomes of the workshop:

The key outcomes of the workshop are as follows:

- Enhanced awareness of Artificial Intelligence concepts and their real-world applications.
- Provided exposure to emerging AI tools, technologies, and industry trends.
- Improved AI readiness and understanding of career opportunities in AI and related domains.
- Motivated participants to undertake AI-based research, projects, and innovations.
- Strengthened collaboration between academia and industry through expert interaction and knowledge sharing.





Newspaper Clips:

మిట్స్ కళాశాలలో సివిల్(ఏ.ఐ) 100కే-ఏ.ఐ రెడీనెస్ సెషన్

- హాజరైన ఫుల్ స్టాక్ జాబా ట్రైనర్ ఎం.చంద్రశేఖర్



కృత్రిమ మేధస్సు పై అవగాహన పెంచుకోవాలి



ఆంధ్రప్రభ

కృత్రిమ మేధస్సు రంగంపై విద్యార్థులకు అవగాహన కల్పించాలి



కృత్రిమ మేధస్సుపై అవగాహన పెంచుకోవాలి



ముత్ యూనివర్సిటీలో హైదరాబాద్ సొసైటీ ఆఫ్ ఇండియా చాప్టర్ సహకారంతో..

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తరణం
31/01/2026 | Page No: 2
tharanam.net

మిట్స్ కళాశాలలో సివిల్(ఏ.ఐ) రెడీనెస్ సెషన్ కార్యక్రమం



SKYLINE

ENGLISH DAILY

Simultaneously published from Tirupati, Vijayawada, Visakhapatnam & Hyderabad

VOLUME : 21, NO. 31 TIRUPATI, SATURDAY, JANUARY 31, 2026 PAGES 8 Rs. 1/-

Awareness Program on "CSI AI 100K – AI Readiness Session" at MITS.

Madanapalle, January 30(Skyline daily): The Computer Science and Engineering department at MITS Deemed to be University, near Madanapalle, organized a program titled "CSI AI 100K – AI Readiness Session" in collaboration with the Computer Society of India (CSI) – Hyderabad Chapter. M Chandra Sekhar, a Full Stack Java Trainer from Techs Academy, Bengaluru, participated as the chief guest. Speaking at the event, he stated that this program is part of CSI's goal to train one lakh young people in the field of artificial intelligence and aims to create awareness among students. He said that by dividing students into small groups, focusing on learning useful concepts in a short time, and working on real-life projects, they can gain proficiency in artificial intelligence. This, he added, would enhance the students' thinking abilities tenfold. He mentioned that the program is designed in line with the state and central government's National AI Mission. Students and faculty participating in this program will receive training in new technologies such as Machine Learning, Generative AI, and Quantum Computing, and will be awarded a nationally recognized CSI digital certificate. He emphasized that students have ample job opportunities in IT companies as well as in data analytics, finance, and healthcare sectors, and urged them to take advantage of these opportunities. The Head of the Department, Dr M Sridevi, Coordinator Dr R Nitya, and students participated in the program.



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(Deemed to be University)
Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi
NAAC Accredited with A+ Grade, NIRF India Rankings 2024 - Band: 201-300 (Engg.)
NBA Accredited - B.Tech. (CIVIL, CSE, ECE, EEE, MECH, CST), MBA & MCA

A Report on Five-Day Hands-On Skill Development Program on "MERN Stack from Fundamentals to Industry-Ready Full-Stack Application Development"

Organized by Department of Computer Science & Engineering

In Association with IIC from 03.02.2026 to 07.02.2026.





Report Submitted by: Mr. Anandaraj B, Assistant Professor, Department of Computer Science & Engineering.
Mode of Conduct: Offline
Venue: SRB309
Report Received on 27.02.2026.
Total Participants: 71
Event Overview

Title: "Five-Day Hands-On Skill Development Program on MERN Stack From Fundamentals to Industry-Ready Full-Stack Application Development"
The program commenced at 9:30 AM with a welcome address delivered by **Mr. Anandaraj B, M.Tech., (PhD), Assistant Professor, Department of CSE, MITS**. Then, **Dr. M. Sreedevi, Professor and HoD/CSE**, extended with inagural speech about the program and highlighted the importance of such workshops in building a strong practical foundation in emerging technologies. She encouraged all the students to actively participate, learn new concepts, and explore innovation through hands-on experience. Following this, the session was handed over to **Mr. Neelesh Tiwari,** who guided the participants through engaging, interactive, and practical training program.

Program Objective
The primary objectives of the program were:

- To introduce students to modern full-stack web development practices.
- To provide hands-on experience with the MERN stack ecosystem.
- To bridge the gap between academic curriculum and industry expectations.
- To enable students to develop, test, and deploy real-time applications.
- To improve placement readiness through project-based learning.





Day-Wise Session Topics

Day 1: Foundations of Web Development & MongoDB

Session Topics:

- Introduction to Full-Stack Development
- Understanding Client-Server Architecture
- Introduction to MERN Stack Ecosystem
- JavaScript ES6 Fundamentals
- MongoDB Basics (NoSQL Concepts)
- CRUD Operations in MongoDB
- MongoDB Atlas Setup (Cloud Database)

Hands-On Activities:

- Creating collections and documents
- Performing CRUD operations using MongoDB Shell
- Connecting MongoDB Atlas with local environment

Day 2: Backend Development with Node.js & Express.js

Session Topics:

- Introduction to Node.js Runtime Environment
- Understanding Event Loop and Asynchronous Programming
- Express.js Framework Overview
- RESTful API Design Principles
- Middleware Concept
- Routing and Controllers
- Integration with MongoDB using Mongoose

Hands-On Activities:

- Building REST APIs
- Creating User Authentication APIs
- Testing APIs using Postman
- Error Handling and Validation

Day 3: Frontend Development with React.js

Session Topics:

- Introduction to React.js
- Component-Based Architecture
- JSX and Virtual DOM
- Props and State Management
- React Hooks (useState, useEffect)
- API Integration using Axios
- Conditional Rendering and Forms Handling

Hands-On Activities:

- Creating dynamic UI components
- Connecting frontend with backend APIs
- Implementing Login and Registration Interface

Day 4: Full-Stack Integration & Authentication

Session Topics:

- Connecting React Frontend with Express Backend
- JWT (JSON Web Token) Authentication
- Role-Based Access Control
- Protected Routes
- Environment Variables & Configuration
- Project Folder Structure (Industry Standard)

Hands-On Activities:

- Implementing Login Authentication
- Securing APIs
- Building Dashboard Interface
- Debugging Full-Stack Applications

Day 5: Deployment & Mini Project Development

Session Topics:

- Deployment Concepts (Frontend & Backend)
- Hosting on Vercel / Render / Netlify
- Database Deployment using MongoDB Atlas
- CI/CD Overview
- Resume Optimization with Projects
- Interview Preparation Guidance

Capstone Mini Project:
Students developed a Full-Stack Web Application, such as:

- Student Management System
- Placement Tracker
- Task Management App
- Mentor-Mentee Interaction Portal

Final Program Outcome
By the end of the program, the participants were able to:

- Design and develop full-stack applications independently.
- Create RESTful APIs using Express.js.
- Build responsive front-end interfaces using React.
- Implement authentication using JWT.
- Deploy applications to cloud platforms.
- Demonstrate project experience suitable for campus placements.

The program ended with a Vote of Thanks delivered by **Mrs. P. Jayaselvi M.E.,(PhD).**, expressing gratitude to the resource person, participants, Head of the Department, Principal, and Management for their support and for making the event a grand success.

A Report on
Two Days Professional Development Programme Hands on Training titled "Series : -I Advanced Java for Industry-Ready Application Development"
Organized by Department of Computer Science & Engineering
from 05.02.2026 to 06.02.2026





Report Submitted by: Dr. R. Sudhakar, Associate Professor, Department of Computer Science & Engineering & Dr. G. Arun Kumar, Associate Professor, Department of Computer Science & Engineering.
Resource Person Details: Ms. R. Kavya , Senior Technical Trainer , Rampex Technologies, Chennai
Time: 10:00 AM to 4: 00 PM
Mode of Conduct: Offline
Report Received on 28.02.2026.

Report of the Event :

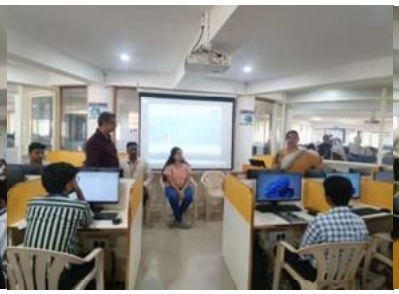
The Department of Computer Science & Engineering organized a Two Days Professional Development Programme **– Hands on Training titled "Series – I: Advanced Java for Industry-Ready Application Development**" on 05 February 2026 and 06 February 2026 for II-Year CSE students. The programme aimed to equip students with advanced Java programming skills aligned with industry requirements.

The inaugural session commenced at 10:00 AM on Day 1 with a brief introduction highlighting the importance of advanced Java technologies in modern software development and enterprise application design.

Dr. R. Sudhakar, Associate Professor, Department of CSE, delivered the welcome address and emphasized the importance of mastering advanced programming concepts to meet the evolving demands of the IT industry.

Professor & Head of the Department – CSE, Dr. M. Sreedevi, addressed the gathering and encouraged students to actively participate in the hands-on sessions to enhance their practical knowledge and technical competence.

Dr. G. Arunkumar, Associate Professor, Department of CSE, introduced the resource person and highlighted the relevance of advanced Java frameworks and backend development skills in campus placements and industry roles.


DAY – 1 TECHNICAL SESSION

Ms. R. Kavya initiated the session with an overview of Advanced Java concepts and their applications in enterprise-level software systems.
The topics covered on Day 1 included:

- Review of Core Java fundamentals
- Object-Oriented Programming principles in depth
- Exception Handling and Best Practices
- Multithreading Concepts
- Collections Framework and Generics
- File Handling and I/O Streams

The session was highly interactive, with students working on practical coding exercises. The resource person demonstrated real-time coding examples and encouraged students to debug and optimize their programs.
Students were guided on:

- Writing clean and efficient code
- Handling runtime errors effectively
- Implementing multithreading applications
- Working with Java Collections for data management

Hands-on practice sessions enabled students to implement concepts immediately after explanation.

DAY – 2 TECHNICAL SESSION

The second day focused on enterprise application development using Advanced Java technologies.
Topics covered included:

- JDBC (Java Database Connectivity)
- Connecting Java Applications with MySQL Database
- CRUD Operations using JDBC
- Introduction to Servlets and JSP
- MVC Architecture
- Mini Project Development (Database-Driven Application)

The resource person demonstrated how to:

- Establish database connections
- Execute SQL queries through Java
- Handle user input via Servlets
- Integrate backend logic with frontend interface
- Deploy and test web-based applications

Students actively developed a mini project under guidance, implementing a simple database-driven web application.
The practical exposure helped students understand real-time project structure and deployment practices followed in the industry.

QUESTION & ANSWER SESSION

Both days concluded with interactive Question & Answer sessions. Students raised queries related to:

- Multithreading implementation challenges
- Database connectivity errors
- Debugging techniques
- Industry expectations for Java Developers
- Career opportunities in Backend Development

Ms. R. Kavya addressed all queries patiently and provided practical tips to improve coding efficiency and interview performance.

Vote of Thanks

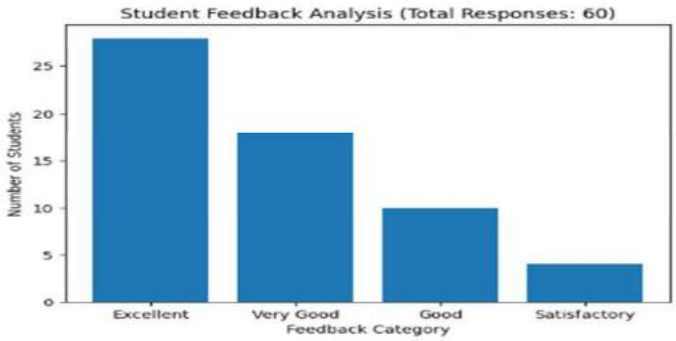
The programme concluded with a Vote of Thanks delivered by Dr. G. Arunkumar, Programme Coordinator and Associate Professor, Department of CSE. He expressed sincere gratitude to the Management, Principal, Head of the Department, Resource Person, faculty members, and students for their active participation and support in making the programme successful.

Outcomes of the Programme

1. **Enhanced Advanced Java Knowledge:** Students gained in-depth understanding of multithreading, collections, JDBC, Servlets, and JSP.
2. **Improved Practical Coding Skills:** Hands-on sessions strengthened students' ability to write, debug, and optimize Java programs.
3. **Industry-Oriented Application Development Skills:** Students learned to develop database-driven web applications using real-time methodologies**.**
4. **Better Understanding of Backend Development:** Exposure to MVC architecture and enterprise-level programming enhanced their readiness for industry roles.
5. **Increased Confidence for Placements:** The training boosted students' confidence in attending technical interviews and coding assessments.

Student Feedback About the Programme

Students expressed that the two-day hands-on training was highly informative, practical, and industry-oriented. They appreciated the real-time coding demonstrations, mini project development, and interactive doubt-clearing sessions. Many students stated that the programme significantly improved their understanding of advanced Java concepts and boosted their confidence in backend development.


Student Feedback Analysis (Total Responses: 60)
Number of Students
0
5
10
15
20
25
Excellent
Very Good
Good
Satisfactory
Feedback Category

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(Deemed to be University)
Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi
NAAC Accredited with A+ Grade, NIRF India Rankings 2024 - Band: 201-300 (Engg.)
NBA Accredited - B.Tech. (CIVIL, CSE, ECE, EEE, MECH, CST), MBA & MCA

A Report on 15 days Skill Development Program on "AWS – Amazon Web Services – Global Certificate Training Programme" Organized by Department of Computer Science & Engineering from 04.03.2026 to 18.03.2026





Report Submitted by: Dr. G. ArunKumar, Associate Professor, Department of Computer Science & Engineering
Resource Person Details: Mr. Karanam Guruteja, Sr. Technical Trainer, GUVI-HCL, Chennai.
Venue: Scaleup Room
Time: 09:00 AM to 04:00 PM
Mode of Conduct: Offline
Attendees Count: 32
Report Received on 31.03.2026.

Resource Person Profile:

Mr. Karanam Guruteja is **a Senior Technical Trainer at GUVI–HCL**, Chennai, with over 15 years of experience in technical training and leadership development. He has extensive experience in delivering industry-oriented training programs for students, colleges, and corporate professionals. Mr. Guruteja has successfully trained more than 1.2 lakh students across 100+ colleges and universities, along with several corporate training programs. His expertise includes Cloud Computing, DevOps, Artificial Intelligence, Cybersecurity, and Database Technologies. With strong communication and training skills, he is committed to equipping learners with industry-relevant skills for career growth and placement success.

Inauguration Details

The 15-Day Skill Development Program on AWS commenced with a formal inauguration ceremony. The dignitaries invited to the dais included **Dr. R. Sudhakar, Associate Professor, CSE; Dr. G. ArunKumar, Associate Professor, CSE; Dr. M. Sreedevi, Professor and Head, CSE; Dr. P. Ramanathan, Principal**; and the **Resource Person, Mr. Karanam Guruteja, Senior Technical Trainer – GUVI-HCL**, Chennai.

The program began with the lighting of the lamp, followed by a prayer song invoking divine blessings. The welcome address was delivered by **Dr. R.sudhakar**, after which **Dr. M. Sreedevi** addressed the gathering and highlighted the importance of skill development in emerging technologies. The presidential address was delivered by **Dr. P. Ramanathan**, who encouraged students to utilize such training programs to enhance their technical competencies. Subsequently, the resource person was formally introduced, and the session was handed over to **Mr. Karanam Guruteja**, who commenced the technical session on the Skill Development Program on AWS.





Objectives of the Program

The major objectives of organizing this Skill Development Program were:

- To introduce students to the **fundamentals of cloud computing and AWS services**.
- To provide **hands-on training** on cloud infrastructure and service deployment.
- To prepare students for the **AWS Global Certification Examination**.
- To enhance **technical competency and industry readiness** among students.
- To create awareness about **cloud architecture, security, and best practices**.
- To strengthen students' profiles for **better career opportunities and placements**.

Training Modules Covered

The training program covered several important AWS concepts and services essential for beginners in cloud computing. Some of the key modules covered during the program include:

- Overview of Amazon Web Services (AWS)
- AWS Global Infrastructure and Architecture
- Identity and Access Management (IAM)
- Elastic Compute Cloud (EC2)
- Amazon Simple Storage Service (S3)
- Virtual Private Cloud (VPC)
- Database Services in AWS
- Cloud Security and Best Practices
- Deployment and Scaling Concepts

The programme was designed to bridge the gap between academic learning and industry requirements by equipping students with in-demand cloud skills through structured sessions, hands-on labs, and real-time applications.

Day 1–2: The programme commenced with an introduction to cloud computing concepts, including types of cloud models (Public, Private, Hybrid) and service models such as IaaS, PaaS, and SaaS. Students were introduced to AWS, its global infrastructure, and the significance of cloud technologies in modern IT industries.

Day 3–4: The sessions focused on AWS Identity and Access Management (IAM) and security fundamentals. Students learned how to create users, roles, and policies, ensuring secure access to AWS resources. Basic hands-on exercises were conducted to reinforce the concepts.

Day 5–6: Students explored core compute services, particularly Amazon EC2 (Elastic Compute Cloud). They learned how to launch, configure, and manage virtual servers, along with understanding instance types, key pairs, and security groups.

Day 7–8: The focus shifted to storage services such as Amazon S3 (Simple Storage Service) and EBS (Elastic Block Store). Students performed practical tasks like creating buckets, uploading data, and managing storage classes.

Day 9–10: Networking concepts in AWS were covered, including Virtual Private Cloud (VPC), subnets, routing tables, and internet gateways. Students gained hands-on experience in designing a basic cloud network architecture.

Day 11–12: Database services such as Amazon RDS (Relational Database Service) and DynamoDB were introduced. Students learned about database creation, configuration, and management in cloud environments.

Day 13: The session emphasized monitoring and management tools such as Amazon CloudWatch and AWS CloudTrail. Students understood how to track system performance and maintain logs for security and auditing purposes.

Day 14: Students were introduced to deployment and automation tools, including AWS Elastic Beanstalk and basic DevOps practices. They learned how to deploy simple applications in the cloud environment.

Day 15: The final day included a revision session, assessment, and project demonstration. Students showcased their understanding by implementing mini-projects based on AWS services. Feedback was collected, and guidance was provided for pursuing AWS certification.

Throughout the programme, expert sessions were delivered by industry professionals, including Mr. Karanam Guruteja from HCL Technologies and GUVI, Chennai. The training methodology combined theoretical knowledge with practical implementation, ensuring active student participation.

Benefits to Students

The AWS Global Certificate Training Program provided several benefits to the participants. Students gained exposure to **industry-relevant cloud technologies**, which are highly valued in the current IT job market.
Some of the key benefits include:

- Understanding of **modern cloud computing concepts**
- Exposure to **global industry standards** in cloud technologies
- Preparation for **AWS certification examinations**
- Enhancement of **technical and practical skills**
- Improved **career prospects and placement opportunities**

The training also encouraged students to explore **advanced cloud computing domains** such as cloud architecture, DevOps, and cloud security.

Program Outcomes (POs) Covered

- **PO1 – Engineering Knowledge:** Students applied computing and analytical knowledge to understand AI, digital platforms, and data-driven approaches in opportunity, training, and start-up decision-making.
- **PO3 – Design/Development of Solutions:** Students learned to design effective opportunity and training strategies for technology-based start-up challenges.
- **PO5 – Modern Tool Usage:** Participants were introduced to modern tools, AI-based analytics platforms, and digital technologies used in training and opportunity management.
- **PO8 – Individual and Team Work:** Group discussions and activities enhanced teamwork and individual problem-solving abilities.
- **PO9 – Communication:** The program improved students' communication skills through presentations and professional discussions.
- **PO11 – Life-Long Learning:** Exposure to current industry practices encouraged continuous learning and adaptability in evolving technological environments.

Valedictory Function

The Valedictory Ceremony of the 15-Day Skill Development Program on AWS, conducted from 04.03.2026 to 18.03.2026, was held with great enthusiasm marking the successful completion of the training program.

The dignitaries invited to the dais included **Dr. R. Sudhakar, Associate Professor, CSE; Dr. G. ArunKumar,** Associate Professor, CSE; **Dr. M. Sreedevi, Professor and Head, CSE**; **Dr. P. Ramanathan, Principal**; and the Resource Person, **Mr. Karanam Guruteja, Senior Technical Trainer – GUVI-HCL**, Chennai.

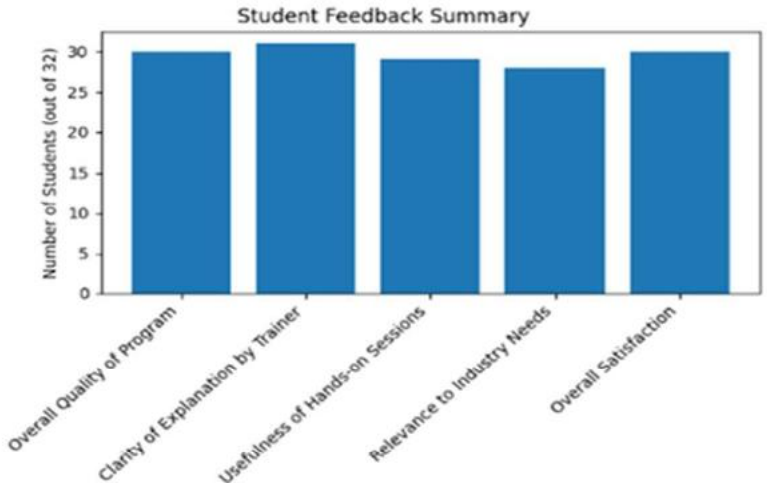
During the valedictory session, the dignitaries appreciated the active participation of the students throughout the program and highlighted the importance of cloud computing technologies in the current IT industry. They encouraged students to continue learning advanced technologies and utilize the knowledge gained from the training for their professional growth.

The resource person, **Mr. Karanam Guruteja**, shared his reflections on the program and appreciated the enthusiasm, dedication, and learning attitude of the participants during the 15-day training sessions.

The program concluded by a vote of thanks acknowledging the support of the management, resource person, faculty members, and participants for making the program a successful one.

Feedback Summary:

1. **Overall Quality of the Program –** Majority of students selected *Agree* and *Strongly Agree*.
2. **Clarity of Explanation by Trainer –** Very positive response indicating effective delivery.
3. **Usefulness of Hands-on Sessions –** Highly appreciated by students.
4. **Relevance to Industry Needs –** Most students agreed the program is industry relevant.
5. **Overall Satisfaction –** High satisfaction level among participants.



Department of Computer Science & Engineering

List of Student Internships at NITs and NITTTR for the Academic Year 2025–2026

S.No.	Register No.	Student Name	Company Name	Start Date	End Date
1	22691A0571	HARSHAVARDHAN VIJAPURAM	NIT, SILCHAR	June 1, 2025	July 31, 2025
2	22691A0599	KEERTHANA EVANJILIN MOLATHATI	NIT, SILCHAR	June 1, 2025	July 31, 2025
3	22691A05B5	MAHESH KUMAR VAILETI	NIT, SILCHAR	June 1, 2025	July 31, 2025
4	22691A05I1	ENDULURI REDDY ROHITH	NIT, SILCHAR	June 1, 2025	July 31, 2025
5	23691A0505	SHAIK AFTAB ALI	NITTTR, Chennai	June 16, 2025	July 26, 2025
6	23691A0598	SYED MALEEN TAJ	NITTTR, Chennai	June 16, 2025	July 26, 2025
7	23691A0599	BYSANI MANASA	NITTTR, Chennai	June 16, 2025	July 26, 2025
8	23691A05C4	YARRAMREDDY NIKHITHA REDDY	NITTTR, Chennai	June 16, 2025	July 26, 2025
9	23691A05H6	SHAIK SADIYA TABASUM	NITTTR, Chennai	June 16, 2025	July 26, 2025
10	24695A0524	YASWANTH KUMAR AKULA	NITTTR, Chennai	June 16, 2025	July 26, 2025
11	23691A05K5	SIRI N	NIT, Puducherry	June 16, 2025	Aug. 16, 2025
12	23691A05N1	PANDIPATI UJWALA	NIT, Puducherry	June 16, 2025	Aug. 16, 2025
13	24695A0520	SOWSHILYA YARASANI	NIT, Puducherry	June 16, 2025	Aug. 16, 2025

HoD/CSE

Head of the Department
Computer Science & Engineering
Madanapalle Institute of Technology & Science
MADANAPALLE-517 325

IEEE

SILCHAR SUBSECTION

IEEE Silchar Subsection Student Summer Internship Program - 2025 (IS3IP-2025)
Silchar, Cachar, Assam-788010

CERTIFICATE

of participation

This is to certify that ***Mr. Harshavardhan Vijapuram*** from Madanapalle Institute of Technology & Sciences has successfully completed the IEEE Summer Internship 2025 under the mentorship of

Dr. Sreenu Sreekumar, Asst. Prof. in the Department of Electrical Engineering, National Institute of Technology Silchar from 1st June 2025 to 31st July 2025.

Date: 10.10.2025
Place: Silchar

Dr. Koena Mukherjee
SECRETARY

Dr. Devendra Gurjar
VICE CHAIR

Prof. Binoy Krishna Roy
CHAIR

IEEE

SILCHAR SUBSECTION

IEEE Silchar Subsection Student Summer Internship Program - 2025 (IS3IP-2025)
Silchar, Cachar, Assam-788010

CERTIFICATE

of participation

This is to certify that ***M Keerthana Evanjilin*** from Madanapalle Institute of Technology & Sciences has successfully completed the IEEE Summer Internship 2025 under the mentorship of

Dr. Sreenu Sreekumar, Asst. Prof. in the Department of Electrical Engineering, National Institute of Technology Silchar from 1st June 2025 to 31st July 2025.

Date: 10.10.2025
Place: Silchar

Dr. Koena Mukherjee
SECRETARY

Dr. Devendra Gurjar
VICE CHAIR

Prof. Binoy Krishna Roy
CHAIR

IEEE

SILCHAR SUBSECTION

IEEE Silchar Subsection Student Summer Internship Program - 2025 (IS3IP-2025)
Silchar, Cachar, Assam-788010

CERTIFICATE

of participation

This is to certify that ***Mr. Vaileti Mahesh Kumar*** from Madanapalle Institute of Technology & Sciences has successfully completed the IEEE Summer Internship 2025 under the mentorship of

Dr. Sreenu Sreekumar, Asst. Prof. in the Department of Electrical Engineering, National Institute of Technology Silchar from 1st June 2025 to 31st July 2025.

Date: 10.10.2025
Place: Silchar

Dr. Koena Mukherjee
SECRETARY

Dr. Devendra Gurjar
VICE CHAIR

Prof. Binoy Krishna Roy
CHAIR

IEEE

SILCHAR SUBSECTION

IEEE Silchar Subsection Student Summer Internship Program - 2025 (IS3IP-2025)
Silchar, Cachar, Assam-788010

CERTIFICATE

of participation

This is to certify that ***Mr. Enduluri Reddy Rohith*** from Madanapalle Institute of Technology & Sciences has successfully completed the IEEE Summer Internship 2025 under the mentorship of

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Place: Silchar

Dr. Koena Mukherjee	Dr. Devendra Gurjar	Prof. Binoy Krishna Roy
SECRETARY	VICE CHAIR	CHAIR

NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING AND RESEARCH, CHENNAI

(Ministry of Education, Government of India)

CERTIFICATE OF INTERNSHIP

Certificate No. IT/2025-26/41

This is to certify that

Mr. AFTAB ALI SHAIK

B. Tech., II year student from the Department of Computer Science Engineering, Madanapalle Institute of Technology and Science, Madanapalle, Andhra Pradesh has successfully completed internship training in **Image Processing (Machine Learning)** at our institute from 16/06/2025 to 26/07/2025. During this period, he was found punctual and hardworking. We wish him success in life.

CHENNAI

26/07/2025

Dean (Academics and Research)

Faculty Mentor

NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING AND RESEARCH, CHENNAI

(Ministry of Education, Government of India)

CERTIFICATE OF INTERNSHIP

Certificate No. IT/2025-26/43

This is to certify that

Ms. SYED MALEEN TAJ

B. Tech., II year student from the Department of Computer Science Engineering, Madanapalle Institute of Technology and Science, Madanapalle, Andhra Pradesh has successfully completed internship training in **Web Design-Department Webpage** at our institute from 16/06/2025 to 26/07/2025. During this period, she was found punctual and hardworking. We wish her success in life.

CHENNAI

26/07/2025

Dean (Academics and Research)

Faculty Mentor

NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING AND RESEARCH, CHENNAI

(Ministry of Education, Government of India)

CERTIFICATE OF INTERNSHIP

Certificate No. IT/2025-26/42

This is to certify that

Ms. MANASA B

B. Tech., II year student from the Department of Computer Science Engineering, Madanapalle Institute of Technology and Science, Madanapalle, Andhra Pradesh has successfully completed internship training in **Web Design-Department Webpage** at our institute from 16/06/2025 to 26/07/2025. During this period, she was found punctual and hardworking. We wish her success in life.

CHENNAI

26/07/2025

Dean (Academics and Research)

Faculty Mentor

NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING AND RESEARCH, CHENNAI

(Ministry of Education, Government of India)

CERTIFICATE OF INTERNSHIP

Certificate No. IT/2025-26/44

This is to certify that

Ms. NIKHITHA REDDY YARRAMREDDY

B. Tech., II year student from the Department of Computer Science Engineering, Madanapalle Institute of Technology and Science, Madanapalle, Andhra Pradesh has successfully completed internship training in **Web Design-Department Webpage** at our institute from 16/06/2025 to 26/07/2025. During this period, she was found punctual and hardworking. We wish her success in life.

CHENNAI

26/07/2025

Dean (Academics and Research)

Faculty Mentor

NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING AND RESEARCH, CHENNAI

(Ministry of Education, Government of India)

CERTIFICATE OF INTERNSHIP

Certificate No. IT/2025-26/45

This is to certify that

Ms. SADIYA TABASUM SHAIK

B. Tech., II year student from the Department of Computer Science Engineering, Madanapalle Institute of Technology and Science, Madanapalle, Andhra Pradesh has successfully completed internship training in **Web Design-Faculty Portfolio** at our institute from 16/06/2025 to 26/07/2025. During this period, she was found punctual and hardworking. We wish her success in life.

CHENNAI

26/07/2025

Dean (Academics and Research)

Faculty Mentor

NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING AND RESEARCH, CHENNAI

(Ministry of Education, Government of India)

CERTIFICATE OF INTERNSHIP

Certificate No. IT/2025-26/46

This is to certify that

Mr. YASWANTH KUMAR AKULA

B. Tech., II year student from the Department of Computer Science Engineering, Madanapalle Institute of Technology and Science, Madanapalle, Andhra Pradesh has successfully completed internship training in **Web Design-Faculty Portfolio** at our institute from 16/06/2025 to 26/07/2025. During this period, he was found punctual and hardworking. We wish him success in life.

CHENNAI

26/07/2025

Dean (Academics and Research)

Faculty Mentor

राष्ट्रीय प्रौद्योगिकी संस्थान पुदुच्चेरी
(शिक्षा मंत्रालय के तहत राष्ट्रीय महत्व का एक संस्थान,भारत सरकार)
तिरुवेट्टाकुड़ी, कारैक्काल - 609 609, केंद्र शासित प्रदेश,पुदुच्चेरी
NATIONAL INSTITUTE OF TECHNOLOGY PUDUCHERRY
(An Institution of National Importance under Ministry of Education, Govt of India)
Thiruvettakudy, Karaikal – 609 609, Union Territory of Puducherry

27/10/2025

CERTIFICATE OF INTERNSHIP

This is to certify that **Ms. Siri Reddy Nandireddy (Reg. No: 23691A05K5),** a third-year B.Tech. student in the Department of Computer Science and Engineering at Madanapalle Institute of Technology & Science, Madanapalle, has successfully completed her full-time internship titled ***"Blockchain Security for Industry Applications."*** The internship took place from 16/06/2025 to 16/08/2025 under the supervision of Dr. Praveen R, Assistant Professor at the National Institute of Technology Puducherry.

Dr. Praveen R
Assistant Professor
Department of CSE
NIT Puducherry

Dr. Madappa VR Sivasubramanian
Professor-In-Charge
Training & Placement
NIT Puducherry

Phone / दूर भाष : 04368-265233 E-mail / ई-मेल : placement@nitpy.ac.in
Web / वेब : www.nitpy.ac.in

राष्ट्रीय प्रौद्योगिकी संस्थान पुदुच्चेरी
(शिक्षा मंत्रालय के तहत राष्ट्रीय महत्व का एक संस्थान,भारत सरकार)
तिरुवेट्टाकुड़ी, कारैक्काल - 609 609, केंद्र शासित प्रदेश,पुदुच्चेरी
NATIONAL INSTITUTE OF TECHNOLOGY PUDUCHERRY
(An Institution of National Importance under Ministry of Education, Govt of India)
Thiruvettakudy, Karaikal – 609 609, Union Territory of Puducherry

27/10/2025

CERTIFICATE OF INTERNSHIP

This is to certify that **Ms. Pandipati Ujwala (Reg. No: 23691A05N1),** a third-year B.Tech. student in the Department of Computer Science and Engineering at Madanapalle Institute of Technology & Science, Madanapalle, has successfully completed her full-time internship titled ***"Blockchain Security for Industry Applications."*** The internship took place from 16/06/2025 to 16/08/2025 under the supervision of Dr. Praveen R, Assistant Professor at the National Institute of Technology Puducherry.

Dr. Praveen R
Assistant Professor
Department of CSE
NIT Puducherry

Dr. Madappa VR Sivasubramanian
Professor-In-Charge
Training & Placement
NIT Puducherry

राष्ट्रीय प्रौद्योगिकी संस्थान पुदुच्चेरी
(शिक्षा मंत्रालय के तहत राष्ट्रीय महत्व का एक संस्थान,भारत सरकार)
तिरुवेट्टाकुड़ी, कारैक्काल - 609 609, केंद्र शासित प्रदेश,पुदुच्चेरी
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(An Institution of National Importance under Ministry of Education, Govt of India)
Thiruvettakudy, Karaikal – 609 609, Union Territory of Puducherry

27/10/2025

CERTIFICATE OF INTERNSHIP

This is to certify that **Ms. Sowshilya Yarasani (Reg. No: 24695A0520),** a third-year B.Tech. student in the Department of Computer Science and Engineering at Madanapalle Institute of Technology & Science, Madanapalle, has successfully completed her full-time internship titled ***"Blockchain Security for Industry Applications."*** The internship took place from 16/06/2025 to 16/08/2025 under the supervision of Dr. Praveen R, Assistant Professor at the National Institute of Technology Puducherry.

Dr. Praveen R
Assistant Professor
Department of CSE
NIT Puducherry

Dr. Madappa VR Sivasubramanian
Professor-In-Charge
Training & Placement
NIT Puducherry

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(UGC-AUTONOMOUS INSTITUTION)

NAAC Accredited with A+ Grade, NIRF India Rankings 2025 - Band: 201-300 (Engg.)
NBA Accredited - B.Tech. (CIVIL, CSE, CST, ECE, EEE, MECH), MBA & MCA

Department of Computer Science & Engineering

Ethnotech In-House Placement Training – Student List 2025–2026

S.NO	ROLL NO	NAME OF CANDIDATE
1	23691A0503	UMESH GARI ABHILASH
2	23691A0504	SHAIK AFSHIYA SULTHANA
3	23691A0507	NUKALA AKHIL
4	23691A0508	DUBBIGALLA AKHILA
5	23691A0510	AMRUTHA LAKSHMI VENNAPUSA
6	23691A0512	P. ANJALI
7	23691A0513	ANJUM SHAIK
8	23691A0515	ARE ASHAMANI
9	23691A0517	R.ASWINI
10	23691A0519	SHAIK BABA SHAREEF
11	23691A0521	RB BHANU
12	23691A0522	KALLURI BHAVYA
13	23691A0524	JAVINEPALLI BHAVYA SREE
14	23691A0525	SIDDARAPU BINDUSREE
15	23691A0526	A.CHAITHRAGNA
16	23691A0527	S.CHARAN KUMAR REDDY
17	23691A0528	PONNIPENTA CHARAN
18	23691A0530	ANNAREDDY CHARITHA
19	23691A0531	P.CHARITHA
20	23691A0533	N DEEKSHITH KUMAR
21	23691A0534	S DEEPA
22	23691A0535	G H DEEPIKA
23	23691A0537	A.DHANUSREE
24	23691A0538	DHEERAJ SREERAMA
25	23691A0540	ELISETTY DINESH KUMAR
26	23691A0542	R.DIVYA SREE
27	23691A0543	A DIVYA TEJA
28	23691A0544	BORAMPALLI FARDEEN KHAN
29	23691A0545	K GANESH
30	23691A0550	MANCHURU GOWTHAM
31	23691A0552	CHINTA GREESHMA
32	23691A0553	PULLAREDDYGARI GUNASREE
33	23691A0555	A. GURU YASASWINI
34	23691A0556	CHAKALA GURU CHANDRA
35	23691A0557	C.HARIKA
36	23691A0559	HARIKAPALETI
37	23691A0560	T HARISH KUMAR REDDY
38	23691A0561	R HARISH
39	23691A0562	SONNAPAREDDY HARSHITHA
40	23691A0563	KOLIMI HEENA KOUSAR
41	23691A0564	BANDARI HEMALATHA
42	23691A0568	KRISH SUDAN
43	23691A0569	KURUVA HEMANTH KUMAR
44	23691A0570	S HIMA BINDU
45	23691A0571	KARNATAKA JAHNAVI
46	23691A0572	P.JAHNAVI
47	23691A0573	JACK JEEVAN RITHISH
48	23691A0575	M.JITHENDRANADH
49	23691A0576	K.JOGENDRA KARTHIKEYA
50	23691A05F5	WUNAKAL PRAVARSHA
51	23691A05P0	S.VIJAYALAKSHMI
52	23691A05P3	K. VINAY

53	23691A05Q0	YASEEN
54	23691A05Q2	REVANURU YASHASWINI
55	24695A0501	AYESHA PINJARI
56	24695A0507	LAKSHMI CHAITHANYA B
57	24695A0508	G MAHENDRA
58	24695A0509	KASTHURI MAHESH KUMAR
59	24695A0510	MAMATHA VULAVABEETI
60	24695A0511	GORRAPPAGARI MOUNIKA
61	24695A0520	SOWSHILYA YARASANI
62	24695A0522	P.V.DEVASENA
63	23691A0506	S.AJAY
64	23691A0577	SANKARAPU JYOSHNA
65	23691A0580	THUNGA KAVYA YADAV
66	23691A0584	J KEERTHI
67	23691A0585	KESAVA REDDY KONDUR
68	23691A0586	PALLA KISHORE
69	23691A0590	DAGGUPATI LAHARI
70	23691A0591	G LAHARI
71	23691A0592	GODDUMARRI LIKITHA
72	23691A0593	KASA LIKITHA
73	23691A0594	V.LOHIVENKAT
74	23691A0596	MALAKA LOKESH REDDY
75	23691A0597	MADHAVI
76	23691A0598	S MALEEN TAJ
77	23691A0599	BYSANI MANASA
78	23691A05A0	MANIKANTA RASALA
79	23691A05A1	CHINTHAKUNTLA MANMOHAN REDDY
80	23691A05A2	MEENAKSHI KAPILI
81	23691A05A3	MUDE MEGHANA
82	23691A05A5	MOHANAPRIYA KANAKA
83	23691A05A6	MOHANA SREE RAMAYANAM
84	23691A05A7	MOKSHITH KONDETI
85	23691A05A8	K MOKSHITHA
86	23691A05A9	S.MOUNISHREDDY
87	23691A05B0	NAMAKARI MUNIKUMAR REDDY
88	23691A05B1	N.MUNVAR BASHA
89	23691A05B2	MOHAMMEDMUSTHAQ T
90	23691A05B3	NAGA HINDUJA THALLAPUREDDY
91	23691A05B4	VASUNURI NAGAIAH
92	23691A05B5	TANGUTURI NANDINI
93	23691A05B6	NANDITHA KATEPALLI
94	23691A05B7	K.NAVAJYOTH
95	23691A05C0	M.NAVYA SREE
96	23691A05C1	NEERAJA DEVARAPALLI
97	23691A05C3	KORRAPATI NIKHIL SHARAN
98	23691A05C4	Y.NIKHITHA
99	23691A05C6	NITHEESHWAR HANUMANTHAKAARI
100	23691A05C7	NOMITHA PATHIVADA
101	23691A05C9	S.NOWSHEEN
102	23691A05D0	RACHAMALLU OMIKA
103	23691A05D2	N OMPRAKASH REDDY
104	23691A05D5	PADMASREE MEENUGURU
105	23691A05D6	ASADI PALLAVI
106	23691A05D7	D V PALLAVI
107	23691A05D8	C.PARTHASARADHI REDDY
108	23691A05E1	UPPARA PAVANI
109	23691A05E2	PAVITHRA VENKATAGIRI
110	23691A05E6	POOJITHA REDDY DANDU
111	23691A05E7	A. POORNA CHANDU
112	23691A05E9	SADHASINI PRAKASH BABU
113	23691A05F3	DEVIREDDI GARI PRASANTHI
114	23691A05F6	PRAVEEN PAMURU
115	23691A05F7	T PREETHI SREE

116	23691A05F8	YERRAMASU PREM VARDHAN
117	23691A05F9	MADALA RAHUL
118	23691A05H8	V SAI CHETHAN
119	23691A05P1	DASARI VIJAYENDRA KUMAR
120	23691A05P4	S V VINAY TEJA REDDY
121	23691A05P5	CHEJARLA VISHNU
122	23691A05Q6	SYED ABUZAR
123	23691A0514	SHAIK ARFIYA ANJUM
124	23691A0518	GUDIMETLA AYESHA
125	23691A0523	S.BHAVYA REDDY
126	23691A0541	VAILETI DINESH KUMAR
127	23691A0546	PASAVALA GNANA PRASUNA
128	23691A0547	GNANADEEP GIRRAJU
129	23691A0548	KURAPATI GOUTAM KUMAR
130	23691A0549	GUVALLA GOWTHAM
131	23691A0551	NARRA GOWTHAM
132	23691A0565	G HEMALATHA
133	23691A0574	JHONNY DIXIT THALARI
134	23691A0581	R.KAVYASREE
135	23691A0582	KEERTHANA THIRUMALASETTY
136	23691A0583	YEKASI KEERTHANA
137	23691A0587	G.KOUSHIK
138	23691A0595	MUNTHA LOKESH
139	23691A05A4	MOHAMMED FARAZ ATTAR
140	23691A05D1	OMKAR NAIK RAMAVATH
141	23691A05D3	LIKITH KUMAR REDDY SOMUGUTTA
142	23691A05D4	C.MUJAHID
143	23691A05E0	PAVANI B
144	23691A05E3	G.PAWAN
145	23691A05E5	G.POOJITHA
146	23691A05E8	POORNA KASHYAP ALURU
147	23691A05F1	P PRANEETH KUMAR REDDY
148	23691A05H0	RESHMITHA
149	23691A05J7	JANNE SARATH CHANDRAN
150	23691A05L0	P.SREEHARI
151	23691A05L8	TEJASWI YALLALA
152	23691A05L9	NAGURI TEJASWINI
153	23691A05M0	TEJESH ANDALAMALA
154	23691A05M1	TEJESH P
155	23691A05M6	THARUN RANGAREDDYGARI
156	23691A05O3	VENKATESH TENEPALLI
157	23691A05O6	K. VIDYASAGAR RAO
158	23691A05O7	VIGNESH MOKSHAGNA TEJ RATAKONDA
159	23691A05O8	VIGNESH SAJJA
160	23691A05P2	N.VIJAYENDRA
161	23691A05P6	PEDDAKAM VISHNUVARDHAN REDDY
162	23691A05P7	D VIVEK
163	23691A05P9	YAMINI KRISHNA ABBINENI
164	23691A05Q1	BANDI YASHASWINI
165	24695A0503	A GANESH
166	24695A0516	KOTA SAI KUMAR
167	24695A0517	TAMMINENI MEDARI SATHESH KUMAR
168	24695A0518	KALLE SIVA RAMA KRISHNA
169	24695A0519	PATNAM SHAIK SONUSAJITH
170	24695A0524	AKULA YASWANTH KUMAR
171	23691A05G0	KONDURU RAJA KUMAR RAJU
172	23691A05G1	B RAJANI
173	23691A05G3	SHAIK RAZAK
174	23691A05G4	RAMISETTY REDDI VIJAY
175	23691A05G5	A.REDDISWARI
176	23691A05G6	SIGI REDDY MANI BHUSHAN
177	23691A05G9	DEVANGAM REKHA
178	23691A05H1	GODDUVELAGALA ROHITH KUMAR

179	23691A05H2	BIJINEMULA MOHAMMAD ROSHAN
180	23691A05H3	C ROSHINI PRIYA
181	23691A05H4	RUSHI VARDHAN REDDY
182	23691A05H5	SYED SADEEFA SULTANA
183	23691A05H6	SHAIK SADIYA TABASUM
184	23691A05H7	Y N SAGAR
185	23691A05H9	M M SAI GANESH
186	23691A05I0	SAI JYOTHI G
187	23691A05I3	VEDASISTLA VENKATA SAI PAVAN KUMAR
188	23691A05I5	K SAI PRASUNA
189	23691A05I6	D.SAI UPENDRA REDDY
190	23691A05I7	PATHAN SAIF KHAN
191	23691A05I8	PATHAN SANA ANJUM
192	23691A05I9	SANA FARHATH KASALA
193	23691A05J0	GULURI SANDHYA
194	23691A05J1	SANDRA S NAIR
195	23691A05J3	SHAIK SANOBAR SULTHANA
196	23691A05J4	GADDE SANTHOSH
197	23691A05J5	OARSU SANTOSH KUMAR
198	23691A05J6	VENKATAPURAM SARALA
199	23691A05J8	KURAPATI SATHVIKA
200	23691A05J9	KISANNAGARI SHASHANK
201	23691A05K2	M SHWETHAANJALI
202	23691A05K3	NALLAPAREDDY SILPA
203	23691A05K4	MUDE SIREESHA
204	23691A05K6	THANGELLA SIVA GANESH
205	23691A05K7	SIVA PRASAD K
206	23691A05K9	A. SREE CHARANI
207	23691A05L2	S.SREEVALLI
208	23691A05L3	K.SREYA SRI
209	23691A05L5	VONTEDDU SUHITHA
210	23691A05L7	SURESH VAYYAVURU
211	23691A05M2	THAHEERA
212	23691A05M3	K.THANUSHA
213	23691A05M4	K.THANUSREE
214	23691A05M5	THARUN BANDI
215	23691A05M7	M.THULASI
216	23691A05M8	UDAY KIRAN KONANGI
217	23691A05M9	K.UDAY SANKAR
218	23691A05N0	BAYINENI UHA CHOUDARY
219	23691A05N1	P.UJWALA
220	23691A05N5	NUKANABOYINA VAMSI
221	23691A05O2	PADAM VENKATA VINEETH SAI
222	23691A05O4	VENNELA CHAMANCHI
223	23691A05O5	M VENUGOPAL
224	23691A05O9	M VIJAY KRISHNA
225	23691A05Q3	UPPITI YASHASWINI
226	23691A05Q4	KOVVURU YASWANTH REDDY
227	23691A05Q5	SHAIK ZEBNOOR
228	24695A0502	KAMMALA BHANU TEJA
229	24695A0504	PADI GURU VARSHINI
230	24695A0505	BALGONDLA HARI SHANKAR
231	24695A0514	BUKKE RAGHAVENDRA NAIK
232	24695A0515	SUDA RUPESH
233	24695A0525	POOLA YESTEBA
234	23691A05F4	Y. PRASHANTH KUMAR REDDY
235	23691A05K5	NANDIREDDY SIRI REDDY
236	23691A05L4	K.DEVI SRI SAMHITA
237	23691A05P8	SHAIK VOSHAN FAROOK
238	24695A0521	YADDANAPUDI TEJASWINI
239	23691A0511	PALAGIRI ANEESHA
240	23691A0558	N.M.HARIKA
241	23691A0578	KARTHIK REDDY VANKAMADDI

242	23691A05F0	ANS PRANAV CHAITANYA
243	23691A05G8	REETHIKA
244	23691A05I4	MEKALA SAI PRASAD
245	23691A05J2	SANJANA BUDHA
246	24695A0506	PULLA KARTHIK
247	23691A0505	SHAIK AFTAB ALI
248	23691A0532	B.CHETHAN
249	23691A05C5	G NISHANT KUMAR
250	23691A05D9	PAVAN KUMAR
251	23691A05N4	MITTAPALEM VAMSI KRISHNA
252	23691A05N9	VEERA PRASAD.C
253	23691A05O1	C VENKATA HEMANTH KUMAR
254	24695A0513	PURUSHOTHAM B
255	23691A05K1	SHAIK SHIFA

HoD/CSE

Head of the Department
Computer Science & Engineering
Madanapalle Institute of Technology & Science
MADANAPALLE-517 325.

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE

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Department of Computer Science & Engineering

List of GATE – 2026 Qualified Students

S.No.	Roll No.	Student Name	Qualified Paper	Gate Registration No.
1	22691A05M8	TANU SHREE MAAMIDIKUNTLA	CS	CS26S37133038
2	22691A05M8	TANU SHREE MAAMIDIKUNTLA	DA	DA26S37133037
3	22691A5K10	SHOURIKA YENDRATHI	CS	CS26S47129130
4	23691A05N8	VEEKSHITHA CHOWDARY V	CS	CS26S47103092
5	23691A05C6	NITHEESHWAR HANUMANTHAKAARI	CS	CS26S47131147
6	23691A05N1V	UJWALA PANDIPATI	CS	CS2647103061
7	22691A05B5	MAHESH KUMAR VAILETI	CS	CS26S47129165
8	23691A05B0	MUNIKUMAR REDDY NAMAKARI	CS	CS26S37131108
9	22691A05M1	SURYA KIRAN BIRRU	CS	CS26S47118140
10	22691a05I5	P. RUTHIK	CS	CS26S47131122
11	22691A05N1	MUDDALA TEJASH	CS	CS26S47118196
12	23691A05C7	NOMITHA PATHIVEDA	CS	CS26S37134087
13	23691A05K7	SIVA PRASAD KORAKUTI	CS	CS26S47129182
14	23691A05J1	SANDRA S NAIR	CS	CS26S37132044
15	22691A05M3	SWARNA LATHA GAVVALA	CS	CS26S37132162

HoD/CSE

Head of the Department
Computer Science & Engineering
Madanapalle Institute of Technology & Science
MADANAPALLE-517 325.

GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD



M. TanuShree

A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **TANU SHREE MAAMIDIKUNTLA**

Name of the Parent/Guardian: **MAAMIDIKUNTLA VASUDEVA KUMAR**

Registration Number: **CS26S37133038**

Date of Birth: **April 24, 2005**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **442**

Marks out of 100: **37.85**

All India Rank (AIR) in the test paper: **12794**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

This Score Card is valid up to 31st March 2029

adadbe1b6472e6387e0d4391a395afd8

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD



M. TanuShree

A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **TANU SHREE MAAMIDIKUNTLA**

Name of the Parent/Guardian: **MAAMIDIKUNTLA VASUDEVA KUMAR**

Registration Number: **DA26S87133037**

Date of Birth: **April 24, 2005**

Test Paper: **Data Science and Artificial Intelligence (DA)**

Date of Examination: **February 15, 2026**

GATE Score: **397**

Marks out of 100: **30.33**

All India Rank (AIR) in the test paper: **6574**

Number of candidates appeared for the test paper : **69242**

Qualifying Marks

General: **26.4**

EWS/OBC-NCL: **23.7**

SC/ST/PwD: **17.5**

This Score Card is valid up to 31st March 2029

de32c8096ea4960584facbc034b1fdc4

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be: Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$, where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

Qualifying in GATE 2026 does not guarantee admission to a postgraduate program or scholarship/financial assistance. Admitting institutes may conduct additional tests or interviews for final selection of candidates.

Graduate Aptitude Test in Engineering (GATE) 2026 was organized by Indian Institute of Technology Guwahati on behalf of National Coordination Board (NCB) - GATE for the Department of Higher Education, Ministry of Education (MOE), Government of India.

GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD



Y. Shourika

A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **SHOURIKA YENDRATHI**

Name of the Parent/Guardian: **YENDRATHI SUDHAKAR**

Registration Number: **CS26S47129130**

Date of Birth: **June 26, 2005**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **302**

Marks out of 100: **25.9**

All India Rank (AIR) in the test paper: **44450**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

This Score Card is valid up to 31st March 2029

61e2bc42bdae41bec0c21e58469d4023

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

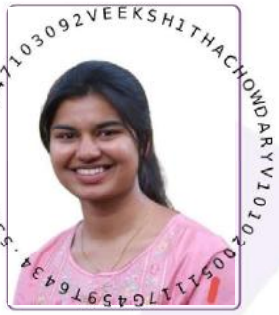
where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

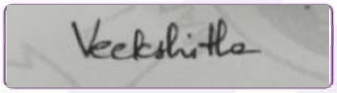
GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD





A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **VEEKSHITHA CHOWDARY V**

Name of the Parent/Guardian: **V ANANDA NAIDU**

Registration Number: **CS26S47103092**

Date of Birth: **November 17, 2005**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **403**

Marks out of 100: **34.53**

All India Rank (AIR) in the test paper: **18020**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

This Score Card is valid up to 31st March 2029

ce4e3c32ec14b7f37c3b95afed118208

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD



H. Nitheeshwar

A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **NITHEESHWAR HANUMANTHAKAARI**

Name of the Parent/Guardian: **CHANDRA SEKHAR BABU HANUMANTHAKAARI**

Registration Number: **CS26S47131147**

Date of Birth: **June 20, 2007**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **439**

Marks out of 100: **37.64**

All India Rank (AIR) in the test paper: **12990**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

This Score Card is valid up to 31st March 2029

568ac01edaf822687b0ab98250a1d760

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD



P. Ujwala

A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **UJWALA PANDIPATI**

Name of the Parent/Guardian: **P GIRISH**

Registration Number: **CS26S47103061**

Date of Birth: **August 6, 2006**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **294**

Marks out of 100: **25.21**

All India Rank (AIR) in the test paper: **47720**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

This Score Card is valid up to 31st March 2029

9f2119ecd2bd542515b641c13951f730

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD



Name of the Candidate: **VAILETI MAHESH KUMAR**

Name of the Parent/Guardian: **V SURYA NARAYANA**

Registration Number: **CS26S47129165**

Date of Birth: **April 2, 2004**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **455**

Marks out of 100: **39.02**

All India Rank (AIR) in the test paper: **11288**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

This Score Card is valid up to 31st March 2029

7fd3ee3c749cb8c6d3ac9c3053a6f729

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD



Name of the Candidate: **MUNIKUMAR REDDY NAMAKARI**

Name of the Parent/Guardian: **NAMAKARI ESWAR REDDY**

Registration Number: **CS26S37131108**

Date of Birth: **July 2, 2006**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **385**

Marks out of 100: **32.96**

All India Rank (AIR) in the test paper: **21041**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

This Score Card is valid up to 31st March 2029

1c471f9475481c4ac25d03bd71ba9974

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

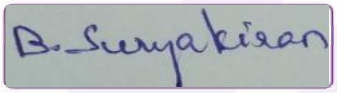
GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD





A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **BIRRU SURYA KIRAN**

Name of the Parent/Guardian: **BIRRU ADINARAYANA**

Registration Number: **CS26S47118140**

Date of Birth: **January 12, 2005**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **334**

Marks out of 100: **28.67**

All India Rank (AIR) in the test paper: **33516**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

This Score Card is valid up to 31st March 2029

9f76656Odd9bb08f7a0331b476aab86

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD



Name of the Candidate: **RUTHIK POLERU**

Name of the Parent/Guardian: **POLERU CHINNA REDDAPPA**

Registration Number: **CS26S47131122**

Date of Birth: **October 15, 2004**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **254**

Marks out of 100: **21.76**

All India Rank (AIR) in the test paper: **68787**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

This Score Card is valid up to 31st March 2029

ad2a8e0ae0b40d4434beb768a95817ca

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD



M.Tejesh

A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **MUDDALA TEJESH**

Name of the Parent/Guardian: **M SUDHAKAR**

Registration Number: **CS26S47118196**

Date of Birth: **May 16, 2005**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **367**

Marks out of 100: **31.43**

All India Rank (AIR) in the test paper: **24672**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

This Score Card is valid up to 31st March 2029

d2285a3336d6e97ddcd337f29ad76461

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

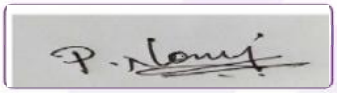
GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD





A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **NOMITHA PATHIVADA**

Name of the Parent/Guardian: **BALARAM PATHIVADA**

Registration Number: **CS26S37134087**

Date of Birth: **January 25, 2006**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **331**

Marks out of 100: **28.4**

All India Rank (AIR) in the test paper: **34140**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

This Score Card is valid up to 31st March 2029

32e099fd15e3b5f691e45ffb1c46afb7

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD



K. Siva Prasad

A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **SIVA PRASAD KORAKUTI**

Name of the Parent/Guardian: **GURUMURTHY KORAKUTI**

Registration Number: **CS26S47129182**

Date of Birth: **October 29, 2005**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **383**

Marks out of 100: **32.81**

All India Rank (AIR) in the test paper: **21411**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

This Score Card is valid up to 31st March 2029

d55b6aeb7282a11a25f31e6a9b8c4c98

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD



Sandra . S . Nair

A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **SANDRA S NAIR**

Name of the Parent/Guardian: **T SINDHU**

Registration Number: **CS26S37132044**

Date of Birth: **June 9, 2005**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **366**

Marks out of 100: **31.33**

All India Rank (AIR) in the test paper: **25131**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

This Score Card is valid up to 31st March 2029

c2d2a9c353cdbae8c350cc117ef9f30b

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

Qualifying marks for GENERAL category, $M_q = \max(25, \min(40, \mu + \sigma))$

where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.

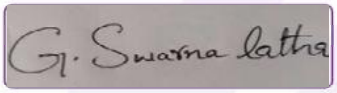
GRADUATE APTITUDE TEST IN ENGINEERING 2026

अभियांत्रिकी स्नातक अभिक्षमता परीक्षा 2026

Organizing Institute : INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

SCORE CARD





A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this scorecard.

Name of the Candidate: **GAVVALA SWARNA LATHA**

Name of the Parent/Guardian: **GAVVALA MALLIKARJUNA**

Registration Number: **CS26S37132162**

Date of Birth: **February 17, 2005**

Test Paper: **Computer Science and Information Technology (CS)**

Date of Examination: **February 8, 2026**

GATE Score: **274**

Marks out of 100: **23.51**

All India Rank (AIR) in the test paper: **56901**

Number of candidates appeared for the test paper : **211020**

Qualifying Marks

General: **30.0**

EWS/OBC-NCL: **27.0**

SC/ST/PwD: **20.0**

This Score Card is valid up to 31st March 2029

acfe6feea2785b5c720624e80826f0dd

Prof. Manabendra Sarma
Organizing Chairperson, GATE 2026
On behalf of NCB-GATE, Ministry of Education

GATE SCORE COMPUTATION

The GATE 2026 score is calculated using the formula:

$$\text{GATE Score} = S_q + (S_t - S_q)\frac{M - M_q}{\bar{M}_t - M_q}$$

where,

M is the marks obtained by the candidate in the test paper mentioned on the GATE 2026 Score Card

M_q is the qualifying marks for general category candidates in the test paper

$\bar{M}_t$ is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper (Including all sessions in case of multisession papers)

S_q = 350, is the score assigned to M_q, and

S_t = 900, is the score assigned to $\bar{M}_t$

In the GATE 2026 score formula, the qualifying marks for the general category candidate in each subject will be:

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where μ is the mean and σ is the standard deviation of marks of all the candidates who appeared for the test paper.